

User Manual

MIC-770 V3

Compact Fanless System with 12th Gen Intel[®] Core[™] i CPU Socket (LGA 1700)



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We want you to get the maximum performance from your products. If you run into technical difficulties, we are here to help. For the most frequently asked questions, you can easily find answers in your product documentation. These answers are normally a lot more detailed than the ones we can give over the phone.

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Declaration of Conformity

FCC

This device complies with the requirements in part 15 of the FCC rules:

Operation is subject to the following two conditions:

- This device may not cause harmful interference.
- This device must accept any interference received, including interference that may cause undesired operation.

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this device in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his/her own expense. The user is advised that any equipment changes or modifications not expressly approved by the party responsible for compliance would void the compliance to FCC regulations and therefore, the user's authority to operate the equipment.

Caution! There is danger of a new battery exploding if it is incorrectly installed. Do



not attempt to recharge, force open, or heat the battery. Replace the battery only with the same or equivalent type recommended by the manufacturer. Discard used batteries according to the manufacturer's instructions.

Technical Support and Assistance

- 1. Visit the Advantech website at http://support.advantech.com to obtain the latest product information.
- 2. Contact your distributor, sales representative, or Advantech's customer service center for technical support if you need additional assistance. Please have the following information ready before calling:
 - Product name and serial number
 - Description of your peripheral attachments
 - Description of your software (operating system, version, application software, etc.)
 - A complete description of the problem
 - The exact wording of any error messages

Warnings, Cautions, and Notes



Warning! Warnings indicate conditions that if not observed can cause personal injury.





Caution! Cautions are included to help prevent hardware damage and data losses. For example,



"Batteries are at risk of exploding if incorrectly installed. Do not attempt to recharge, force open, or heat the battery. Replace the battery only with the same or equivalent type as recommended by the manufacturer. Discard used batteries according to the manufacturer's instructions."



Notes provide additional optional information.

Safety Instructions

- 1. Read these safety instructions carefully.
- 2. Retain this user manual for future reference.
- 3. Disconnect the equipment from all power outlets before cleaning. Use only a damp cloth for cleaning. Do not use liquid or spray detergents.
- 4. For pluggable equipment, the power outlet socket must be located near the equipment and easily accessible.
- 5. Protect the equipment from humidity.
- 6. Place the equipment on a reliable surface during installation. Dropping or letting the equipment fall may cause damage.
- 7. The openings on the enclosure are for air convection. Protect the equipment from overheating. Do not cover the openings.
- 8. Ensure that the voltage of the power source is correct before connecting the equipment to a power outlet.
- 9. Position the power cord away from high-traffic areas. Do not place anything over the power cord.
- 10. All cautions and warnings on the equipment should be noted.
- 11. If the equipment is not used for a long time, disconnect it from the power source to avoid damage from transient overvoltage.
- 12. Never pour liquid into an opening. This may cause fire or electrical shock.
- 13. Never open the equipment. For safety reasons, the equipment should be opened only by qualified service personnel.
- 14. If any of the following occurs, have the equipment checked by service personnel:
 - The power cord or plug is damaged.
 - Liquid has penetrated the equipment.
 - The equipment has been exposed to moisture.
 - The equipment is malfunctioning, or does not operate according to the user manual.
 - The equipment has been dropped and damaged.
 - The equipment shows obvious signs of breakage.
- 15. Do not leave the equipment in an environment with a storage temperature of below -40°C (-40°F) or above 85°C (185°F) as this may damage the components. The equipment should be kept in a controlled environment.
- 16. CAUTION: Batteries are at risk of exploding if incorrectly replaced. Replace only with the same or equivalent type as recommended by the manufacturer. Discard used batteries according to the manufacturer's instructions.
- 17. In accordance with the IEC 704-1:1982 specifications, the sound pressure level at the operator's position should not exceed 70 dB (A).
- 18. RESTRICTED ACCESS AREA: The equipment should only be installed in a Restricted Access Area.

DISCLAIMER: These instructions are provided according to IEC 704-1 standards. Advantech disclaims all responsibility for the accuracy of any statements contained herein.

Safety Precautions



Warning! Always completely disconnect the power cord from your chassis whenever you work with the hardware. Do not make connections while the power is on. Sensitive electronic components can be damaged by sudden power surges. Only experienced electronics personnel should open the PC chassis.



Caution! Always ground yourself to remove any static charge before touching the motherboard. Modern electronic devices are very sensitive to static electric discharges. As a safety precaution, use a grounding wrist strap at all times. Place all electronic components on a static-dissipative surface or in a static-shielded bag when they are not in the chassis.

Caution! The computer is provided with a battery-powered real-time clock circuit. There is danger of explosion if a battery is incorrectly replaced. Replace only with same or equivalent type recommended by the manufacturer. Discard used batteries according to the manufacturer's instructions.



Caution! There is danger of a new battery exploding if it is incorrectly installed. Do not attempt to recharge, force open, or heat the battery. Replace the battery only with the same or equivalent type recommended by the manufacturer. Discard used batteries according to the manufacturer's instructions.

Packing List

Before system installation, check that the items listed below are included and in good condition. If any item does not accord with the list, contact your dealer immediately.

- 1 x MIC-770 V3 bare-bone system
- Startup Manual EN\CN\TC for MIC-770 V3 Ed.1
- 4-pin Phoenix connectors 2 x mounting brackets
- 1 x SATA cable
- 1 x SATA power cable
- 1 x CPU thermal grease
- 2 x RAM thermal pads

P/N: 2041077050 P/N: 1652003234 P/N: 1960070543T00A P/N: 1700013095-01 P/N: 1700024372-01

- P/N: 2170000093-01
- P/N: 1990019498N000

Ordering Information

Part Number	VGA	HDMI	2.5" HDD /SSD	mSATA	NVMe M.2	USB 3.2 (Gen2)	USB 3.2 (Gen1)	USB 2.0	GbE	сом	PCIe/PCI Exp	Power
MIC- 770V3W -00A1	1	1	1	1	1	2	6	0	2	· ·	i-module (optional)	9~36 V _{DC}
MIC- 770V3H- 00A1	1	1	1	1	0	0	4	4	2	· ·	i-module (optional)	9~36 V _{DC}

* MIC-770V3H does not support MIC-75M20-01 and MIC-75G30.

** Please use the wide-temperature NVMe module without the heatsink.

Optional i-Module

Module Number	Description
4-Slot expansion mod	ule
MIC-75M13-00B1	1 x PCle x16 + 3 x PCl slot i-module
3-Slot expansion mod	ule
MIC-75M40-01A2**	1 x PCle x8 + 2 x PCle x4 slot i-module
2-Slot expansion mod	ule
MIC-75M20-00C1	1 x PCle x16 + 1 x PCle x4 slot i-module
MIC-75M20-01A2**	2 x PCle x8 slot i-module
MIC-75M11-00B1	1 x PCle x16 + 1 x PCl slot i-module
1-Slot expansion mod	ule
MIC-75M10-00A2	1 x PCle x16 slot
Featured expansion m	odule
MIC-75S00-00A1	2 x removable storage
MIC-75S20-00A2**	1 x PCle x16 + 1 x PCle x4 slot + 2 x 2.5" swappable HDD/SSD
MIC-75G20-10B1	1 x PCle x16 (double deck) + 1 x PCle x4 + 2 x 2.5" hot-swap HDD/ SSD GPU slot i-module
MIC-75G30-00B1**	2 x PCle x8 + 1 x PCle x4 slot + 2 x 2.5" swappable HDD/SSD
MIC-75GF10-00A1	MIC-75GF10 MXM i-module, 1 x PCle x16 + 1 x PCle x4
98R17520301	2 x 3.5" HDD kit

* Please refer to the i-Module datasheet for more details.

** R680E SKU only

Optional Accessories

Part Number	Description
96PSA-A230W24P4-3	ADP A/D 100-240V 230W 24V C14 terminal block 4P
1702002600	Power cord (USA) UL/CSA, 3-pin, 10A, 125V, 1.83M, 180D
1700008921	Power cord PSE 3P 7A 125V 183cm
1702002605	Power cord (EU), 3-pin, 10A, 250V 1.83m, 90D
96PSD-A240W24-MN*	A/D 100-240V 240W 24V NDR DIN-rail
1700031170-01	DC-DC power cord, A cable TEM*4/TEM*4 UL2464 18AWG 150cm
1700029720-01	AC-DC power cord (US), M cable AC CONN 3P 183 cm
1700030520-01	AC-DC power cord (CN), M cable conn 3P CCC 10A 250V 150cm
1700031408-01	AC-DC power cord (EU), M cable conn 3P/G-TEM*3 80cm
AIIS-DIO32-00A1E**	AIIS GPIO module (32-bit)
PCA-TPMSPI-00A1	TPM 2.0 module (SPI)
98R17500001	MIC DVI FIO
98R17500101	MIC HDMI/Remote power on/off FIO
98R17500301	MIC HDMI kit FIO
98R17500401	MIC Remote power on/off FIO
98R17500601	MIC COMport kit FIO
98R17500701	MIC remote power on/off kit for SFIO
98R17500802	MIC reset/remote power on/off/5V _{DC} kit FIO
98R17500901	MIC GPIO kit
98R17501001	MIC DisplayPort kit
98R17501101	MIC COM and HDMI kit
98910770301***	MIC NVMe + 4 LAN Advanced FIO
98910770401***	MIC NVMe Advanced FIO
98910770501***	MIC 4 PoE Advanced FIO
98R1752010E	2nd 2.5" HDD/SSD kit (used in 2-slot i-Module)
98R1752020E	MIC Dual SSD kit
98R17500201	MIC DIN-rail mounting kit (90/180)
98R17500210	MIC DIN-rail mounting kit (180)
98R17500501	MIC wall mounting kit
i-Door Module (MOS series module)	Supports i-Door module (MOS series), except PoE. Please refer to the Advantech website below or search "iDoor Mod- ule Mini PCIe Expansion Kit". http://www.advantech.com.tw/products/idoor-module-mini-pcie- expansion-kit/sub_bc858a7f-a52b-441b-a59c-f511289f98bc
i-Door Module (PCM series module)	Supports i-Door module (PCM series), except PoE. Please refer to the Advantech website below: http://www.advantech.com/products/idoor-technology-mini-pcieex- pansion-kit/sub_efdb96af-a8f7-4cde-9592-dbf5c9794d16 Note: A bracket is required to mount the PCM series i-door module. Please refer to P/N: 1960065854N001 i Door_bracket

* Please refer to the Flex I/O datasheet for more details.

** AIIS-DIO32 requires a DB37 bracket (P/N: 1960068787N002)

*** R680E SKU only

i-module Optional Accessories

Part Number	Description
98R1751300E	1 x 8 cm FAN module (for MIC-75M13/75M40/75S20)
98R1752000E	2 x 4 cm FAN module (for MIC-75M20/MIC-75M11)

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General Introduction

This chapter details background information on the MIC-770 V3.

1.1 Introduction

The Advantech MIC-770 V3 is a compact, fanless system that utilizes the latest generation Intel® 14nm platform with the new PCH R680E/H610E on a proprietary form factor motherboard. Its main feature is the "expansion slot module", which lets users integrate the MIC-770 V3 system into various applications when developing an entire industrial computer setup. Furthermore, the MIC-770 V3 can also work as an independent, fanless, compact embedded box computer and supports a wide range of DC power inputs.

The rugged aluminum case provides superior heat dissipation and effectively shields against high EMI, shock, and vibration. The MIC-770 V3 is equipped with a 12th Gen CoreTM i desktop CPU featuring up to 16 cores, making it highly suitable for embedded and industrial PC applications that require high processor performance within limited spaces. The system also features powerful I/O interfaces—including Ethernet, USB 3.2, serial port, and 2 x Mini PCIe.

Furthermore, the MIC-770 V3 (W SKU) adopts an Advantech WISE-iBMC out-ofband management solution, enabling remote power control via an NIC port and LAN2 should abnormal OS operation occur.

1.2 Product Features

1.2.1 General

- **CPU:** 12th Gen Intel[®] Core[™] i CPU socket (LGA1700)
- **PCH:** Intel[®] R680E/H610E
- System Memory: Dual-channel DDR5 4800 MHz, up to 32GB per socket (R680E SKU supports ECC)
- Storage Devices: Supports 1 x drive bay space for SATA 2.5" HDD/SSD (up to 2 x with optional kit)
- NVMe M.2: Supports 1 x M-Key 2280 with PCIe Gen4 x4 (R680E SKU, operating temp. -20 ~ 50°C)
- **mSATA:** Supports 1 x mSATA via Mini PCIe slot
- **Graphic:** VGA + HDMI
- **Ethernet Ports:** 2 x RJ-45
- Watchdog Timer: Single-chip watchdog 255-level interval timer, setup by software
- I/O Interface: 2 x RS-232/422/485 supports auto flow control; 4 x RS-232 via optional cable
- USB:
 - R680E: 2 x USB 3.2 (Gen2), 6 x USB 3.2 (Gen1) and 1 x USB 2.0 (internal)
 - H610E: 4 x USB 3.2 (Gen2), 4 x USB 2.0
- Audio: High Definition Audio (HD), Line-out, Mic-in

Expansion interface:

- R680E: 1 x Mini PCIe, 1 x Mini PCIe/mSATA
- H610E: 1 x Mini PCIe, 1 x mSATA

1.2.2 Display

- Chipset: R680E: Intel[®] UHD Graphics 770
- Resolution:
 - VGA: Supports up to 1920x1200 @60 Hz

- HDMI: Supports up to 4096x2160 @30Hz

1.2.3 Ethernet

- Controller:
 - R680E LAN1: Intel[®] I219LM, LAN2: Intel[®] i210IT (supports iBMC)
 - H610E LAN1: Intel[®] I219V, LAN2: Intel[®] i210IT
- **Speed:** 10/100/1000 Mbps
- Interface: 2 x RJ-45
- Standard: Compliant with IEEE 802.3, IEEE802.3u, IEEE 802.ab

1.2.4 SUSI API

MIC-770 V3 supports SUSI API which helps customers develop their own remote management programs under Windows 10.

1.2.5 WISE-iBMC Out-of-Band Power Management

MIC-770 V3 R680E SKU is equipped with a WISE-iBMC solution which provides out-of-band management for remote power-on/power-off/reset/force-shutdown via LAN2 if abnormal conditions are selected.



The WISE-iBMC power control function is operated on Advantech WISE-DeviceOn software. Before installation and setup of the WISE-Agent program, changing the device IP address to the same as your network IP via a utility tool is necessary. Please refer to the product support website to download the iBMC utility, DeviceOn software, and SOP/ User Manual.

1.3 Chipset

1.3.1 Functional Specifications

1.3.1.1 Processor

Processor 12th Gen Intel[®] Core[™] i CPU socket (LGA1700)

1.3.1.2 Chipset

PCH Intel [®] R680E/H610E chipset Memory Supports DDR5 4800MHz MHz ECC RAM (R680E SKU only) (without ECC) SODIMM Socket: 260-pin SODIMM socket*2 (up to 32GB per socket) Intel [®] UHD Graphics 770 Supports DirectX 12 Supports OpenGL 4.5 Supports Intel [®] Quick Sync Video I/O interface VGA: Supports resolutions up to 1920x1200 @60 Hz (VGA connector: Onboard D-SUB 15P) HDMI: Supports resolutions up to 4096x2160 @30Hz (HDMI Connector: Onboard HDMI) SATA Interface 3 x SATA 3.0 (W SKU), 2 x SATA 3.0 (H SKU) Legacy IED (Including IRQ)/Native AHCI appearance to OS Partial/slumber power management modes with wake
Memory (without ECC) SODIMM Socket: 260-pin SODIMM socket*2 (up to 32GB per socket) Intel [®] UHD Graphics 770 Supports DirectX 12 Supports OpenGL 4.5 Supports Intel [®] Quick Sync Video Integrated Intel HD Graphics - VGA: Supports resolutions up to 1920x1200 @60 Hz (VGA connector: Onboard D-SUB 15P) - HDMI: Supports resolutions up to 4096x2160 @30Hz (HDMI Connector: Onboard HDMI) 3 x SATA 3.0 (W SKU), 2 x SATA 3.0 (H SKU) Legacy IED (Including IRQ)/Native AHCI appearance to OS
 Supports DirectX 12 Supports OpenGL 4.5 Supports Intel[®] Quick Sync Video I/O interface VGA: Supports resolutions up to 1920x1200 @60 Hz (VGA connector: Onboard D-SUB 15P) HDMI: Supports resolutions up to 4096x2160 @30Hz (HDMI Connector: Onboard HDMI) 3 x SATA 3.0 (W SKU), 2 x SATA 3.0 (H SKU) Legacy IED (Including IRQ)/Native AHCI appearance to OS
SATA Interface Legacy IED (Including IRQ)/Native AHCI appearance to OS
Capable of 6Gbit/s transfer rate
USB Interface R680E: 2 x USB 3.2 (Gen2) and 6 x USB 3.2 (Gen1) H610E: 4 x USB 3.2 (Gen2) and 4 x USB 2.0 Supports high-speed, full-speed, and low-speed Supports legacy keyboard/mouse software
Power Supports ACPI 5.0 Management ACPI power management logic supported Power connector: plug-in block 4Px1
BIOS AMI 256Mb (R SKU)/256Mb (H SKU) Flash BIOS via SPI
BIOS AMI 256Mb (R SKLI)/256Mb (H SKLI) Elash BIOS via SPI

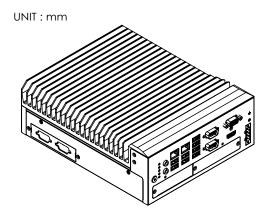
Note! Legacy platform is not supported.

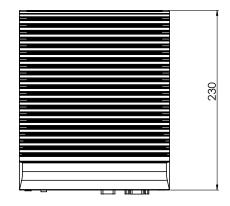
1.3.1.3 Others

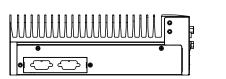
Serial Ports	 Nuvoton NCT 6126D supported Up to 6 serial ports by Nuvoton NCT6126D supported. High speed NS16C550A compatible UARTs with data rates up to 1.5 Mbps. Supports IRQ sharing among serial ports. COM1/2: Supports RS-232/422/485 and setting mode by BIOS with support for auto flow control. COM 3~6: Supports RS-232, via optional cable.
	Serial port connector: D-SUB CON.9P
LAN	 R680E LAN1: Intel[®] I219LM (PHY), LAN2: Intel[®] i210IT I210IT (NIC) (Support iBMC) H610E LAN1: Intel[®] I219V (PHY), LAN2: Intel[®] i210IT I210IT (NIC) Compliant with IEEE 802.3, IEEE 802.3u, IEEE 802.ab. Supports 10/100/1000 Mbps. Supports Wake-on-LAN.
Audio	 Audio Codec: Realtek ALC888S: Compliant with HD Audio specifications. Supports: 16-/20-/24-bit DAC and 16-/20-/24-bit ADC resolution Supports: Line-out, Mic-in DAC supports 16-/20-/24-bit PCM format, multiple stereo recording.
Industrial Features	 Watchdog Timer: Can generate system reset or NC. The watchdog timer is programmable, with each unit equal to one second (255 levels).
Battery Backup	BR2032 3 V/190mAh

1.4 Mechanical Specifications

1.4.1 Dimensions







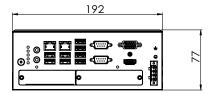


Figure 1.1 MIC-770 V3 Mechanical Dimensions

1.4.2 Weight

2.8 kg (6.17 lb)

1.5 Power Requirements

1.5.1 System Power

 Minimum power input: DC12V (-25%) -30V (+20%), Absolute Maximum Voltage Rating is 9V - 36V.

1.5.2 RTC Battery

BR2032 3 V/190 mAh

1.6 Environmental Specifications

1.6.1 Operating Temperature

-20 ~ 50°C (4 ~ 122°F) (65W CPU) & -20 ~ 60°C (4 ~ 140°F) (35W CPU) with 0.7 m/sec airflow: with 1 x Industrial wide-temp SSD

1.6.2 System Safety Certification Test Temperature

0 ~ 40°C with (32 ~ 104°F) 2.5" SSD

1.6.3 Relative Humidity

■ 95% @40°C (non-condensing)

1.6.4 Storage Temperature

■ -40 ~ 85°C (-40 ~ 185°F)

1.6.5 Vibration During Operation

- When the system is equipped with SSD only: 3 Grms, IEC 60068-2-64, random, 5 ~ 500 Hz, 1 hr/axis, x, y, z, 3 axes.
- When the system is equipped with 2.5" HDD: 1 Grms, IEC 60068-2-64, random, 5 ~ 500 Hz, 1 hr/axis, x, y, z, 3 axes.

1.6.6 Shock During Operation

When the system is equipped with SSD only: 20 G, IEC 60068-2-27, half-sine, 11ms duration.

1.6.7 Safety

UL/CB, CCC, BSMI

1.6.8 EMC

CE, FCC, CCC, BSMI

MIC-770 V3 User Manual



H/W Installation

This chapter introduces external I/ O and the installation of MIC-770 V3 hardware.

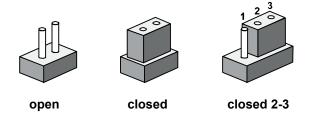
2.1 Introduction

The following sections show the internal jumper settings and the external connectors and pin assignments.

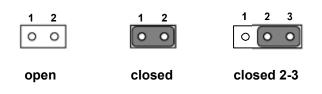
2.2 Jumpers & Slide Switches

2.2.1 Jumper Description

You may configure the MIC-770 V3 to match the needs of your application by setting jumpers. A jumper is a metal bridge used to close an electric circuit. It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To close a jumper, you connect the pins with the clip. To open a jumper, you remove the clip. Sometimes a jumper will have three pins, labeled 1, 2, and 3. In this case you would connect either pins 1 and 2, or 2 and 3.



The jumper settings are schematically depicted in this manual as follows.



A pair of needle-nose pliers may be helpful when working with jumpers. If you have any doubts about the best hardware configuration for your application, contact your local distributor or sales representative before you make any changes. Generally, you simply need a standard cable to make most connections.

2.2.2 Jumper List

Table 2.1: Jumper List		
Label	Function	
JCMOS1	Clear CMOS	
PSON1	System AT/ATX mode option	
JME1	ME jumper mode option	
JWDT1_JOBS1	Watchdog mode option	

2.2.2.1 Clear CMOS

The MIC-770 V3 single board computer contains a jumper that can erase CMOS data and reset the system BIOS information. Normally this jumper should be set with pins 1-2 closed. If you want to reset the CMOS data, set CMOS1 to 2-3 closed for just a few seconds, and then move the jumper back to 1-2 closed. This procedure will reset the CMOS to its default setting.

CMOS1	Clear CMOS
Footprint	3x1 Pin
Setting	Function
(1-2)	Normal (default)
(2-3)	Clear CMOS

2.2.2.2 System AT/ATX Mode Function Option

MIC-770 V3 supports AT or ATX mode and the default is an ATX module. If you want to change to AT mode you can find the AT/ATX mode jumper on the motherboard.

PSON1	System AT/ATX Mode Option
Footprint	3x1 Pin
Setting	Function
(1-2)	AT module
(2-3)	ATX module

2.2.2.3 System ME Mode Function Option

MIC-770 V3 supports ME Enable & Disable and the default is "Disable". If you want to change the ME mode, you can set the ME mode jumper on the motherboard.

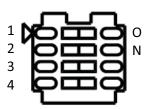
JME1	System ME Mode Option
Footprint	3x1 Pin
Setting	Function
(1-2) (2-3)	ME Enable
(2-3)	ME Disable (default)

2.2.2.4 System Watchdog Mode Function Option

MIC-770 V3 single board computer contains a jumper that can set Watchdog mode.

JWDT1_JOBS1	Watchdog Mode Function Option
Footprint	5x1 pin
Setting	Function
(2-3)	Watchdog
(4-5)	ERR_BEEP

2.2.2.5 USB Standby Power & VGA Always On Setting (DIP Switch)



DIP Switch (SW1)

Switch	State	Setting
SW1-1	1 (default)	USB3C1 does not provide standby charging
	On	USB3C1 provides standby charging
SW1-2	2 (default)	USB3C2 does not provide standby charging
	On	USB3C2 provides standby charging
SW1-3	3 (default)	USB3C3 does not provide standby charging
	On	USB3C3 provides standby charging
SW1-4	4 (default)	VGA is always on
	On	VGA will be ON when a cable is detected

2.3 Connectors

2.3.1 MIC-770 V3 External I/O Connectors

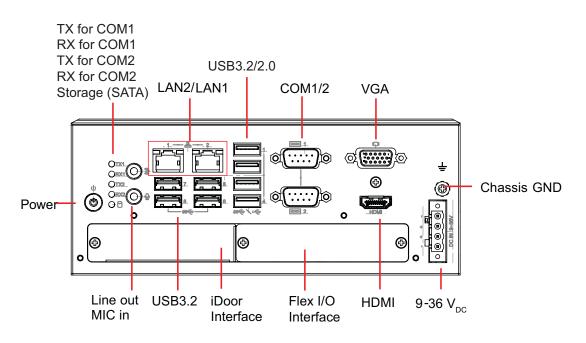


Figure 2.1 MIC-770 V3 Front View

2.3.1.1 COM Connector

MIC-770 V3 provides two 9-pin D-sub connectors, offering RS-232/422/485 serial communication interface ports. The default setting is ES-232, but this can be modified via BIOS settings. You can find detailed setting methods in Chapter 3.

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Table 2.2: COM Connector Pin Assignments			
	RS-232	RS-422	RS-485
Pin	Signal Name	Signal Name	Signal Name
1	DCD	Tx-	DATA-
2	RxD	Tx+	DATA+
3	TxD	Rx+	NC
4	DTR	Rx-	NC
5	GND	GND	GND
6	DSR	NC	NC
7	RTS	NC	NC
8	CTS	NC	NC
9	RI	NC	NC

Note!

NC represents "No Connection".



2.3.1.2 Ethernet Connector (LAN)

MIC-770 V3 is equipped with two Ethernet controllers that are fully compliant with IEEE 802.3u 10/100/1000 Mbps CSMA/CD standards. LAN1 is equipped with Intel® i219 and LAN2 is equipped with Intel® i210. The Ethernet port provides a standard RJ-45 jack connector with LED indicators on the front side to show its Active/Link status and Speed status.

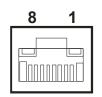


Figure 2.2 Ethernet Connector

Table 2.3: Ethernet Connector Pin Assignments		
Pin	10/100/1000 BaseT Signal Name	
1	TX+	
2	TX-	
3	RX+	
4	MDI2+	
5	MDI2-	
6	RX-	
7	MDI3+	
8	MDI3-	

2.3.1.3 Audio Connector

MIC-770 V3 has two stereo audio ports with phone jack connectors, one Line_Out, one Mic_In. The audio chip is controlled by ALC888S, and it's compliant with AZALIA standards.



Figure 2.3 Audio Connector

Table 2.4: Audio Connector Pin Assignments		
Pin	Audio Signal Name	
1	Line_Out	
2	Mic_In	

Chapter 2 H/W Installation

2.3.1.4 USB 3.0 Connector

MIC-770 V3 provides USB 3.2/3.1 interface connectors, which provide complete Plug & Play and hot swapping for up to 127 external devices. The USB interface complies with USB XHCI, Rev. 3.0. Please refer to the table below for pin assignments.

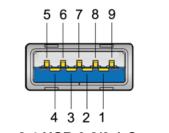


Figure 2.4 USB 3.2/3.1 Connector

Table 2.5: USB 3.0 Connector Pin Assignments		
Pin 1	+5V	
Pin 2	USB Data -	
Pin 3	USB Data +	
Pin 4	GND	
Pin 5	SSRX-	
Pin 6	SSRX+	
Pin 7	GND	
Pin 8	SSTX-	
Pin 9	SSTX+	

2.3.1.5 VGA Connector

The MIC-770 V3 provides a high-resolution VGA interface with a 15-pin D-sub connector to support a VGA CRT monitor. It supports display resolutions of up to 2048x1152 @ 60 Hz.

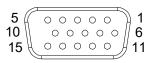


Figure 2.5 VGA Connector

Table 2.6: VGA Connector Pin Assignments					
Pin	Signal Name	Pin	Signal Name		
1	Red	2	Green		
3	Blue	4	NC		
5	GND	6	GND		
7	GND	8	GND		
9	+5V	10	GND		
11	NC	12	DDC_DAT		
13	H-SYNC	14	V-SYNC		
15	DDC_CLK				

2.3.1.6 HDMI Connector

An integrated, 19-pin receptacle connector HDMI Type A interface is provided. The HDMI link supports resolutions of up to 4096x2160 @ 30Hz.

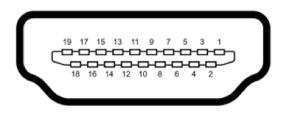


Figure 2.6 HDMI Receptacle Connector

Table 2.7: HDMI Connector Pin Assignments					
Pin	Signal Name	Pin	Signal Name		
1	TMDS Data 2+	2	TMDS Data 2 shield		
3	TMDS Data 2-	4	TMDS Data 1+		
5	TMDS Data 1 shield	6	TMDS Data 1-		
7	TMDS Data 0+	8	TMDS Data 0 shield		
9	TMDS Data 0-	10	TMDS clock+		
11	TMDS clock shield	12	TMDS clock-		
13	CEC	14	Reserved		
15	SCL	16	SDA		
17	DDC/CEC Ground	18	+5V		
19	Hot Plug Detect				

2.3.1.7 Power Input Connector

MIC-770 V3 comes with a four-pin header as default that carries $9V_{DC}$ - $36V_{DC}$ external power input.

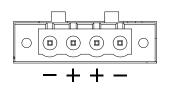


Figure 2.7 4-Pin Header

Table 2.8: Pin Assignments for Power Connector Pin Header				
Pin	Signal Name			
1	GND			
2	+9 V _{DC} ~ 36 V _{DC}			
3	+9 V _{DC} ~ 36 V _{DC}			
4	GND			

Chapter 2 H/W Installation

2.3.1.8 Power ON/OFF Button

MIC-770 V3 comes with a Power On/Off button with LED indicators on the front side to show its On status (Green LED) and Off/Suspend status (RED LED), that supports the dual functions of Soft Power-On/Off (instant off or delay 4 seconds), and Suspend.

Power button LED status: System On: LED On System Suspend: Fast flashes System Off: Off System Off in deep sleep: Off



Figure 2.8 Power Button

2.3.1.9 LED Indicators

MIC-770 V3 provides COM1 & COM2 TX/RX LED for data transmission status monitoring.

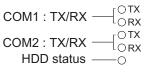
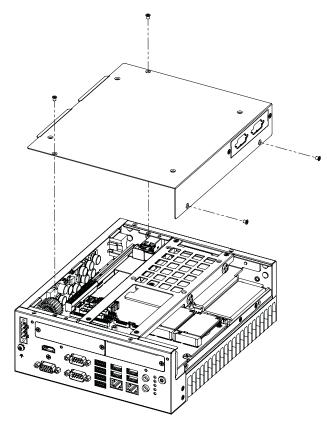


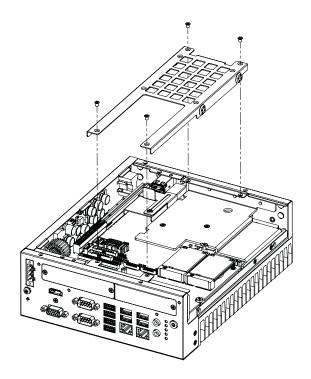
Figure 2.9 LED Indicators

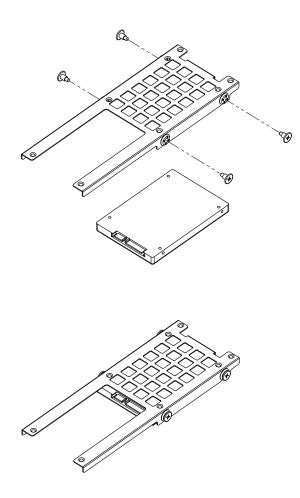
2.4 Installation

2.4.1 HDD Installation

- 1. Undo 4 screws and remove the bottom cover.
- 2. Undo 4 screws to remove the HDD tray.







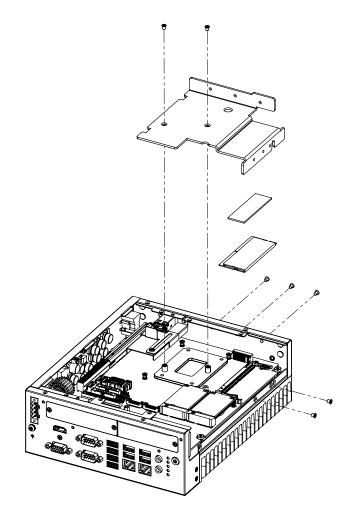
- 3. Secure the HDD with 4 x HDD screws (P/N:1930002235).
- 4. Assemble the SATA cable / power cable and replace the HDD tray; secure with 4 screws.
- 5. Replace the bottom cover.

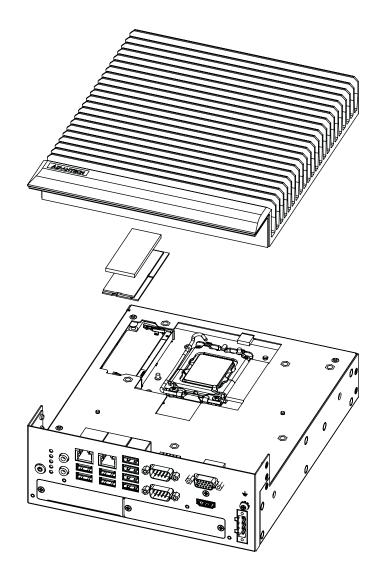
Note! Please refer to the *i*-module Manual for *i*-module assembly.



2.4.2 Memory Installation

- 1. Undo the 4 screws to remove the bottom cover.
- 2. Undo 7 screws to remove the memory thermal cover and install the memory and affix the thermal pad (P/N: 1990019498N000).





- 3. Undo 3 screws to remove the memory thermal cover.
- 4. Affix the thermal pad (P/N: 1990019498N000) to the memory, and reassemble the memory.



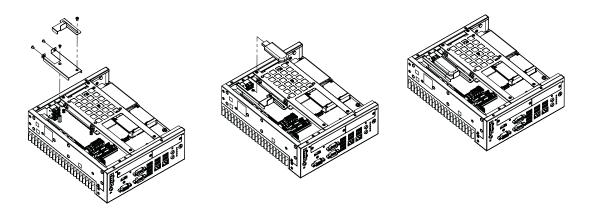
The thermal pad and memory thermal cover must be completely covered and secured.

2.4.3 m-SATA/Mini-PCle Installation

- R680E: 1 x Mini PCIe (via Nano-SIM), 1 x Mini PCIe/mSATA
- H610E: 1 x Mini PCIe (via Nano-SIM), 1 x mSATA
- 1. Undo 4 x screws and remove the bottom cover.
- 2. Install the module in Mini PCIe socket 1, or m-SATA in Mini PCIe socket 2 and secure with screws.
- 3. Replace the bottom cover and secure it with the screws.

2.4.4 Internal USB 2.0 Installation (R SKU Only)

- 1. Undo 4 x screws and remove the bottom cover.
- 2. Loosen the screws and adjust the bracket size in accordance with the USB dongle size.
- 3. Replace the bottom cover and secure with screws.



2.4.5 COM 3/4/5/6 Port Installation

MIC-770 V3 supports 2 x standard RS-232/422/485 serial ports. If more serial ports are needed, MIC-770 V3 is expandable for up to 4 x serial ports by Flex I/O 98R17500601 (RS-232 only).

- 1. Replace the baffle plate for COM 3/4 and COM5/6.
- 2. Remove the HDD tray.
- 3. Assemble the DB9 connector and baffle, and install the cable for the COM3/4 and COM5/6 connectors.
- 4. Secure the COM module in the system and replace the HDD tray/bottom cover.

2.4.6 Expansion Module Installation (Optional)

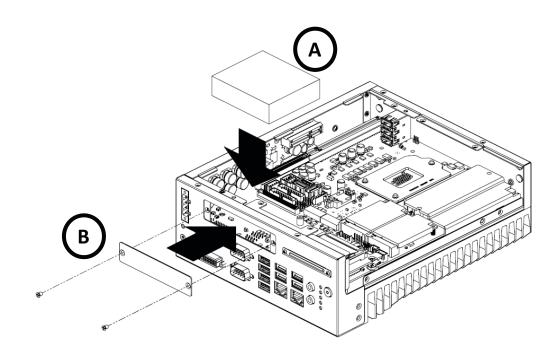
MIC-770 V3 supports the following optional modules for different applications.

A: Expansion module:

- 1. 98R17500001 MIC DVI FIO
- 2. 98R17500101 MIC HDMI/Remote power on/off FIO
- 3. 98R17500301 MIC HDMI kit FIO
- 4. 98R17500401 MIC Remote power on/off FIO
- 5. 98R17500601 MIC COM port kit FIO
- 6. 98R17500701 MIC Remote power on/off kit for SFIO
- 7. 98R17500801 MIC Reset/Remote power on/off/5V_{DC} kit FIO
- 8. 98R17500901 MIC GPIO kit FIO
- 9. 98R17501001 MIC DP kit FIO
- 10. AIIS-DIO32-00A1E** AIIS GPIO module (32-bit)
- 11. PCA-TPMSPI-00A1 TPM 2.0 module (SPI)
- ** AIIS-DIO32 requires a DB37 bracket (P/N: 1960068787N002)

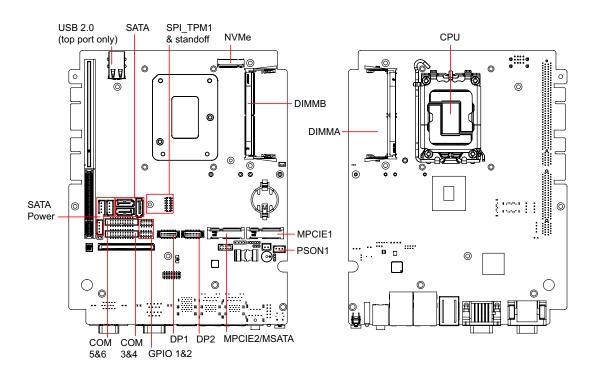
B: Bracket for expansion modules

- 1. Undo the 4 x screws and remove the bottom cover.
- 2. Undo the HDD tray & expansion module baffle.



- 3. Remove the baffle cover.
- 4. Assemble the module on the motherboard (Note: Optional expansion modules need to be connected with a cable. (Please refer to MB internal I/O connector specifications on the I/O connector page for GPIO connectors.)
- 5. Assemble the module baffle with screws.
- 6. Replace the bottom cover and secure with screws.

2.4.7 MIC-770 V3 MB I/O Connector



MIC-770 V3 User Manual



BIOS Operation

3.1 Introduction

With the AMI BIOS Setup utility, you can modify BIOS settings and control the specific features of your computer. The Setup utility uses a number of menus for making changes and turning the specific features on or off. This chapter describes the basic navigation of the MIC-770 V3 setup screens.

Main Advanced Chipset Security	Aptio Setup – AMI Boot Save & Exit MEBx	
BIOS Information BIOS Vendor Core Version Compliancy Project Version Build Date and Time Access Level Main Board	American Megatrends 5.0.2.7 0.07 x64 UEFI 2.8; PI 1.7 M770v3HWF60X201 06/29/2023 11:12:00 Administrator MIC-770V3W	Set the Date. Use Tab to switch between Date elements. Default Ranges: Year: 1998–9999 Months: 1–12 Days: Dependent on month Range of Years may vary.
Serial Number UUID System Date <mark>System Time</mark> Power Type	Default string 0002000300040005 0006000700080009 [Mon 01/15/2024] [16:21:57] ATX	++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version	2.22.1288 Copyright (C) 2023	

Figure 3.1 Main Setup Screen

AMI's BIOS ROM has a built-in setup program that allows users to modify the basic system configuration. This information is stored in the NVRAM area so that it retains the setup information when the power is turned off.

3.2 Entering BIOS Setup

Press at bootup to enter the AMI BIOS Setup utility. The Main Menu will appear on the screen. Use the arrow keys to select among the items and press <Enter> to accept or enter the sub-menu.

3.2.1 Main Menu

When users first enter the BIOS setup utility, they enter the Main setup screen. Users can always return to the Main setup screen by selecting the Main tab. There are two Main Setup options described in this section. The Main BIOS Setup screen is shown below.

Main Advanced Chipset Security	Aptio Setup – AMI Boot Save & Exit MEBx	
BIOS Information BIOS Vendor Core Version Compliancy Project Version Build Date and Time Access Level Main Board	American Megatrends 5.0.2.7 0.07 x64 UEFI 2.8; PI 1.7 M770V3HWF60X201 06/29/2023 11:12:00 Administrator MIC-770V3W	Set the Date. Use Tab to switch between Date elements. Default Ranges: Year: 1998–9999 Months: 1–12 Days: Dependent on month Range of Years may vary.
Serial Number UUID	Default string 0002000300040005 0006000700080009	++: Select Screen
System Date System Time Power Type	[Mon 01/15/2024] [16:22:01] ATX	<pre>fl: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit</pre>
Version	2.22.1288 Copyright (C) 202	3 AMI

Figure 3.2 Main Setup Screen

The Main BIOS setup screen has two main frames. The left frame displays all the options that can be configured. Grayed-out options cannot be configured; options in blue can. The right frame displays the key legend.

The key legend above is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it.

System Time / System Date

Use this option to change the system time and date. Highlight System Time or System Date using the <Arrow> keys. Enter new values through the keyboard. Press the <Tab> key or the <Arrow> keys to move between fields. The date must be entered in MM/DD/YY format. The time must be entered in HH:MM:SS format.

3.2.2 Advanced BIOS Features Setup

Select the Advanced tab from the MIC-770 V3 setup screen to enter the Advanced BIOS setup screen. You can select any of the items in the left frame of the screen, such as CPU configuration, to go to the sub-menu for that item. You can display an Advanced BIOS Setup option by highlighting it using the <Arrow> keys. All Advanced

BIOS Setup options are described in this section. The Advanced BIOS Setup screens are shown below. The sub-menus are described on the following pages.

Aptio Setup – AMI Main <mark>Advanced</mark> Chipset Security Boot Save & Exit MEBx	
 Platform Misc Configuration CPU Configuration Power & Performance PCH-FW Configuration Trusted Computing ACPI Settings SMART Settings NCT6126D Super IO Configuration Hardware Monitor SS RTC Wake Settings iBMC Configuration Serial Port Console Redirection Intel TXT Information PCI Subsystem Settings USB Configuration Network Stack Configuration NCME Configuration Driver Health 	System ACPI Parameters. ++: Select Screen 1: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version 2.22.1288 Copyright (C) 20	23 AMI

Figure 3.3 Advanced BIOS Features Setup Screen

3.2.2.1 iBMC Configuration

Advanced	Aptio Setup – AMI	
iBMC Out-of-Band Management		iBMC function control
iBMC Vendor ID iBMC Device ID iBMC Version iBMC BIOS Capability	13FE 4000 1401 00000007	
iBMC Apacer Coresnapshot BIOS POST STEP Recovery mode Change bootorder	[Enabled] Not Found [Enabled] [Enabled] [Enabled]	
		<pre>++: Select Screen f↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit</pre>
Versio	on 2.22.1288 Copyright (C) 20	23 AMI

Figure 3.4 iBMC Configuration

■ iBMC

Enable/Disable the iBMC controller's hardware communication. The default setting is "Enabled". The iBMC controller/function can be disabled if the item is selected as "Disabled".

3.2.2.2 Platform Misc Configuration

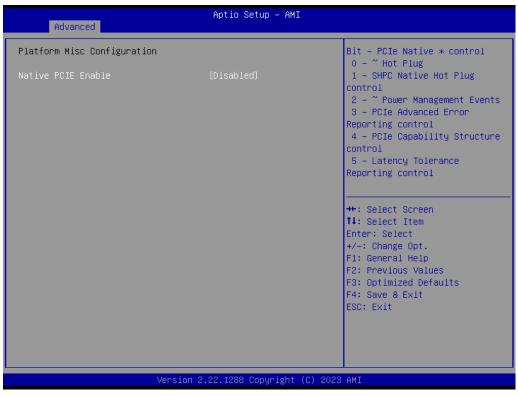


Figure 3.5 Platform Misc Configuration

Native PCIE Enable

Enable/Disable PCI Express native support.

3.2.2.3 CPU Configuration

Advanced	Aptio Setup – AMI	
CPU Configuration		▲ Enable/Disable Software Guard
CFO CONTIGUIATION		Extensions (SGX)
Туре	Intel(R) Core(TM)	
	i9-10900E CPU @ 2.80GHz	
ID	0xA0654	
Speed	2800 MHz	
L1 Data Cache	32 KB × 10	
L1 Instruction Cache	32 KB × 10	
L2 Cache	256 KB × 10	
L3 Cache	20 MB	
L4 Cache	N/A	
VMX SMX/TXT	Supported	
SMXZIXI	Supported	→+: Select Screen
Software Guard Extensions (SGX)	[Disabled]	14: Select Item
Hardware Prefetcher	[Enabled]	Enter: Select
Adjacent Cache Line Prefetch	[Enabled]	+/-: Change Opt.
Intel (VMX) Virtualization	[Enabled]	F1: General Help
Technology		F2: Previous Values
Active Processor Cores	[A11]	F3: Optimized Defaults
Hyper-Threading	[Enabled]	F4: Save & Exit
AES	[Enabled]	ESC: Exit
Intel Trusted Execution Technology	[Disabled]	
Alias Check Request	[Disabled]	
DPR Memory Size (MB)	4	▼

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	Aptio Setup – AMI	
Advanced		
Type ID Speed L1 Data Cache L1 Instruction Cache L2 Cache L3 Cache L4 Cache VMX SMX/IXT	Intel(R) Core(TM) i9-10900E CPU @ 2.80GHz 0xA0654 2800 MHz 32 KB x 10 32 KB x 10 256 KB x 10 20 MB N/A Supported Supported	▲ Reset TPM Aux content. Txt may not functional after AUX content gets reset.
Software Guard Extensions (SGX) Hardware Prefetcher Adjacent Cache Line Prefetch Intel (VMX) Virtualization Technology Active Processor Cores Hyper-Threading AES Intel Trusted Execution Technology Alias Check Request DPR Memory Size (MB) Reset AUX Content	[Disabled] [Enabled] [Enabled] [Enabled] [A11] [Enabled] [Enabled] [Disabled] [Disabled] [Disabled] 4 [no]	<pre>++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit</pre>
Version 2	2.21.1278 Copyright (C) 202	O AMI

Figure 3.6 CPU Configuration

Software Guard Extensions (SGX)

Enable/Disable or Intel[®] Software Guard Extensions.

Hardware Prefetcher

The Hardware Prefetcher uses a technique that fetches instructions and/or data from memory into the CPU cache memory well before the CPU needs it to improve the load-to-use latency. You may choose to enable or disable it.

Adjacent Cache Line Prefetch

The Adjacent Cache-Line Prefetch mechanism, like automatic hardware prefetch, operates without programmer intervention. When it is enabled through the BIOS, two 64-byte cache lines are fetched into a 128-byte sector, regardless of whether the additional cache line has been requested or not. You may choose to enable or disable it.

■ Intel[®] Virtualization Technology

This feature is used to enable or disable the $Intel^{(R)}$ Virtualization Technology (IVT) extension. It allows multiple operating systems to run simultaneously on the same system by creating virtual machines, each running its own x86 operating system.

Active Processor Core

Use this item to select the number of processor cores you want to activate when you are using a multi-core processor.

Hyper-Threading

Enable/Disable Intel[®] Hyper-Threading technology.

AES

Enable/Disable CPA advanced encryption standard instructions.

Intel[®] Trusted Execution Technology

Enable/Disable utilization of additional hardware capabilities provided by Intel® Trusted Execution Technology. Changes require a full power cycle to take effect.

Rest AUX Content

Reset TPM AUX content. TXT may not be functional after AUX content gets reset.

3.2.2.4 Power & Performance



Figure 3.7 Power & Performance



Figure 3.8 CPU - Power Management Control

Power Config

Default is Max. TDP. It enables the user to adjust CPU TDP to 35W or 15W according to the user's requirement.

Boot Performance

Select the performance state that the BIOS will set before OS handoff.

Intel[®] Speedstep[™]

Allows more than two frequency ranges to be supported.

Turbo Mode

Enable/Disable processor turbo mode.

C states

Intel[®] C states setting for power saving.

3.2.2.5 PCH-FW Configuration

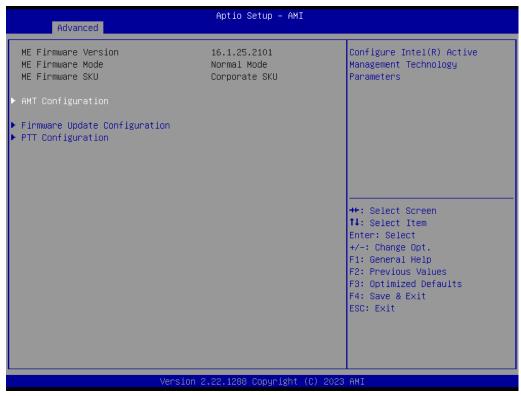


Figure 3.9 PCH-FW Configuration

PCH-FW Version

PCH-FW page shows $Intel^{\ensuremath{\mathbb{R}}}$ ME FW information.

AMT Configuration

 ASF Configuration Secure Erase Configuration 	
1 F F F F	nable/Disable of AMT USB Provisioning.
	<pre> +: Select Screen 14: Select Item inter: Select +/-: Change Opt. 51: General Help 52: Previous Values 53: Optimized Defaults 54: Save & Exit 550: Exit</pre>
۔ Version 2.22.1288 Copyright (C) 2023 A	AMI

Figure 3.10 AMT Configuration

CIRA Configuration

Aptio Setup Utility - Advanced	Copyright (C) 2016 American	Megatrends, Inc.
Activate Remote Assistance Process CIRA Timeout	[Disabled] O	Trigger CIRA boot Note: Network Access must be activated first from MEBx Setup.
		<pre>++: Select Screen f↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit</pre>
Version 2.18.1263. Co	opyright (C) 2016 American M	ESC: Exit egatrends, Inc.

Figure 3.11 CIRA Configuration

 Activate Remote Assistance Process Trigger CIRA boot.

ASF Configuration

Advanced	Aptio Setup – AMI	
PET Progress WatchDog OS Timer BIOS Timer ASF Sensors Table	[Enabled] [Disabled] 0 [Disabled]	Enable/Disable PET Events Progress to receive PET Events.
	Version 2.22.1288 Copyright (C) 2023 AMI

Figure 3.12 ASF Configuration

- PET Progress

Enable/Disable PET events progress to receive PET events.

- Watchdog
- Enable/Disable the Watchdog Timer.

- ASF Sensors Table

Enable/Disable to add the ASF Sensor Table to the ASF ACPI Table.

Chapter 3 BIOS Operation

Secure Erase Configuration

Advanced	Aptio Setup – AMI	
Secure Erase mode Force Secure Erase	[Simulated] [Disabled]	Change Secure Erase module behavior: Simulated: Performs SE flow without erasing SSD Real: Erase SSD. **** If SATA device is used, OEM could use SECURE_ERASE_HOOK_PROTOCOL to remove SATA power to skip G3 cycle. ***
		<pre>++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit</pre>
	Version 2.22.1288 Copyright (C	C) 2023 AMI

Figure 3.13 Secure Erase Configuration

- Secure Erase mode

Change Secure Erase module behavior to "Simulated" or "Real".

- Force Secure Erase

Enable/Disable force Secure Erase on the next boot.

OEM Flag Settings

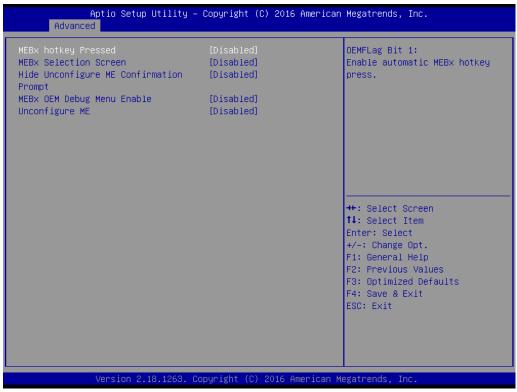


Figure 3.14 OEM Flag Settings

- MEBx hotkey Pressed

Enable/Disable" automatic MEBx hotkey press.

MEBx Selection Screen

Enable/Disable MEBx Selection Screen.

- Hide Un-Configure ME Confirmation Prompt

Hide Un-Configure ME without password confirmation prompt.

- MEBx OEM Debug Menu Enable

Enable/Disable OEM debug menu in MEBx.

- Unconfigure ME

Un-Configure ME without password.

MEBx Resolution Settings

Advanced		American Megatrends, Inc.
Non-UI Mode Resolution UI Mode Resolution Graphics Mode Resolution	[Auto] [Auto] [Auto]	Resolution for non-UI text mode.
		++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit

Figure 3.15 MEBx Resolution Settings

- Non-UI Mode Resolution

Set resolution for non-UI text mode.

- UI Mode Resolution

Set resolution for UI text mode.

- Graphics Mode Resolution

Set resolution for graphics mode.

Firmware Update Configuration

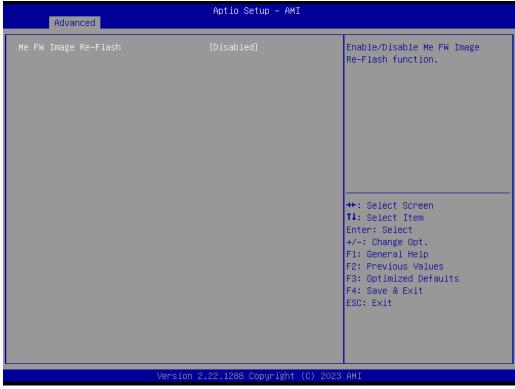


Figure 3.16 Firmware Update Configuration

- ME FW Image Re-flash

Enable/Disable ME firmware image re-flash function.

3.2.2.6 Trusted Computing

Advanced	Aptio Setup – AMI	
TPM 2.0 Device Found Firmware Version: Vendor: Security Device Support Active PCR banks Available PCR banks	600.18 INTC [Enable] SHA256 SHA256,SHA384,SM3	Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available.
SHA256 PCR Bank SHA384 PCR Bank SM3_256 PCR Bank Pending operation Platform Hierarchy Storage Hierarchy Endorsement Hierarchy Physical Presence Spec Version TPM 2.0 InterfaceType Device Select	[Enabled] [Disabled] [Disabled] [Enabled] [Enabled] [Enabled] [1.3] [CRB] [Auto]	<pre>++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit</pre>
Version 2.22.1288 Copyright (C) 2023 AMI Figure 3 17 TPM Settings		

Figure 3.17 TPM Settings

TPM Support

Enable/Disable TPM Support. You can purchase the Advantech LPC TPM module to enable the TPM function. P/N: PCA-TPMSPI-00A1.

3.2.2.7 ACPI Settings



Figure 3.18 ACPI Settings

Enable ACPI Auto Configuration

Enable/Disable ACPI auto configuration.

Enable Hibernation

Enable/Disable Hibernation (OS/S4 Sleep State). This option may not be applied in some OS.

- ACPI Sleep State
 "Auto or S1 only" or "S3 only" ACPI Sleep State.
- S3 Video Repost
 Enable/Disable S3 Video Repost.

3.2.2.8 SMART Settings



Figure 3.19 SMART Settings

SMART Self Test

Enable/Disable SMART Self Test on all HDDs during POST.

3.2.2.9 Super IO Configuration

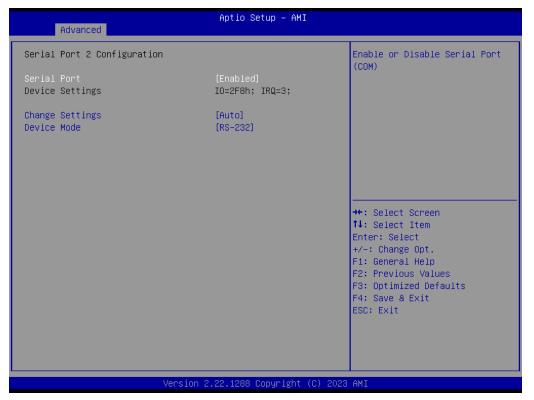
MIC-770 V3 supports 2 x RS-232/422/485 on the front side. MIC-770 has 4 more RS-232 (Serial Ports 3, 4, 5, 6) via 2 x DB9 cables in the accessory box.

NCT6126D Super IO Configuration Set Parame Super IO Chip NCT6126D > Serial Port 1 Configuration > Serial Port 2 Configuration	ters of Serial Port
Super IO Chip NCT6126D ▶ Serial Port 1 Configuration	
 Serial Port 2 Configuration Serial Port 4 Configuration Serial Port 5 Configuration Serial Port 6 Configuration 	
++: Select 11: Select Enter: Sel +/-: Chang F1: Genera F2: Previo F3: Optimi F4: Save & ESC: Exit Version 2.22.1288 Copyright (C) 2023 AMI	Item ect e Opt. il Help us Values zed Defaults

Figure 3.20 Super IO Configuration



Figure 3.21 Serial Port 1 Configuration





Advanced	Aptio Setup – AMI	
Serial Port 3 Configuration		Enable or Disable Serial Port (COM)
Serial Port Device Settings	[Enabled] IO=3E8h; IRQ=5;	(600)
Change Settings	[Auto]	
		<pre>++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit</pre>
Version 2	2.22.1288 Copyright (C) 2023	AMI

Figure 3.23 Serial Port 3 Configuration

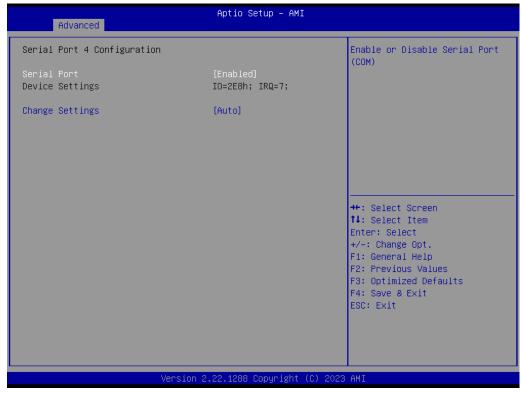


Figure 3.24 Serial Port 4 Configuration

Advanced	Aptio Setup – AMI	
Serial Port 5 Configuration		Enable or Disable Serial Port (COM)
Serial Port	[Enabled]	
Device Settings	IO=220h; IRQ=10;	
Change Settings	[Auto]	
		++: Select Screen
		†↓: Select Item
		Enter: Select +/-: Change Opt.
		F1: General Help
		F2: Previous Values
		F3: Optimized Defaults F4: Save & Exit
		ESC: Exit
Vers	ion 2.22.1288 Copyright (C)	2023 AMI

Figure 3.25 Serial Port 5 Configuration

Advanced	Aptio Setup – AMI	
Serial Port 6 Configuration		Enable or Disable Serial Port
Serial Port Device Settings	[Enabled] IO=228h; IRQ=11;	(COM)
Change Settings	[Auto]	
		++: Select Screen f1: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version 2.22.1288 Copyright (C) 2023 AMI		

Figure 3.26 Serial Port 6 Configuration

Serial Port 1 Configuration

- Serial Port

Enable/Disable Serial Port 1.

Change Settings
 Select an optimal setting for serial port 1.

Serial Port 2 Configuration

- Serial Port
 Enable/Disable Serial Port 2.
- Change Settings
 Select an optimal setting for serial port 2.

Serial Ports 3-6 Configuration

- Serial Port

This item allows users to disable or enable the serial ports.

- Change Settings

This item allows users to change the settings of the serial ports. The default setting is Auto.

3.2.2.10 H/W Monitor

Advanced	Aptio Setup – AMI	
Pc Health Status System Main Board Temperature i-Module Temperature CPU(PECI) Temperature system Power Temperature i-Module SYSFAN1 Speed i-Module SYSFAN2 Speed +12VIN +5VIN +VBAT Fan Configuration Case Open Warning Watchdog Timer CPU(PECI) ACPI Shutdown Temperature	: +39 °C : N/A : +30 °C : +33 °C : N/A : N/A : H12.038 V : +5.184 V : +5.184 V : +2.912 V [Disabled] [Disabled] [Disabled] [Disabled]	Fan Configuration Parameters. ++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version	n 2.22.1288 Copyright (C)	2023 AMI

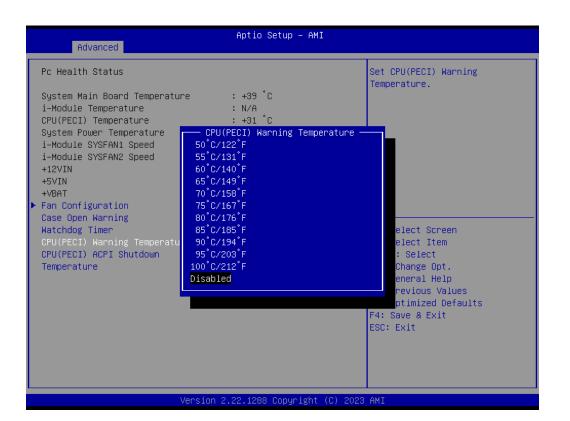
Figure 3.27 PC Health Status

Case Open Warning

Enable/Disable the Chassis Intrusion monitoring function. When it is enabled and the case is opened, the speaker beeps.

CPU (PECI) Warning Temperature

Use this item to set the CPU warning temperature. When the system reaches the warning temperature, the speaker will beep.



ACPI (PECI) Shutdown Temperature

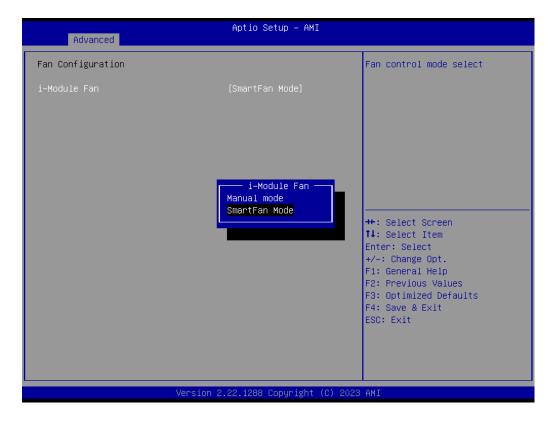
Use this item to set the ACPI shutdown temperature. When the system reaches the shutdown temperature, it will automatically be shut down by ACPI OS to protect the system from overheating damage.

Advanced Pc Health Status System Main Board Temperature : +39 °C i-Module Temperature : N/A CPU(PECI) Temperature : N/A CPU(PECI) Temperature : +31 °C System Power Temperature : +31 °C I-Module SYSFAN1 Speed -CPU(PECI) ACPI Shutdown Temperature -Hodule SYSFAN2 Speed -CPU(FECI) ACPI Shutdown Temperature +V8AT -So Configuration Case Open Harning -So C/140 °F Natchdog Timer -So C/167 °F CPU(PECI) ACPI Shutdown -So C/222 °F 100° C/212 °F -CPU(PECI) ACPI Shutdown Temperature :00° C/212 °F 100° C/212 °F -CPU(PECI) ACPI Shutdown Temperature :00° C/212 °F 100° C/212 °F -CPU(PECI) ACPI Shutdown Temperature :00° C/222 °F 100° C/212 °F -CPU(PECI) ACPI Shutdown Temperature :00° C/212 °F 100° C/212 °F -CPU(PECI) ACPI Shutdown Temperature :00° C/212 °F 10° C/222 °F :00° C/212 °F 10° C/223 °F :00° C/212 °F 10° C/223 °F			
Pc Health Status Set CPU(PECI) ACPI Shutdown Temperature. System Main Board Temperature : +39 °C i-Module Temperature : N/A CPU(PECI) Temperature : +31 °C System Power Temperature : +31 °C System Power Temperature : +31 °C System Power Temperature : +31 °C System Fower Temperature : +31 °C CPU(FECI) ACPI Shutdown Temperature : +31 °C * For Configuration : -50 °C /163 °F CPU(FECI) ACPI Shutdown : -50 °C /194 °F 95 °C /203 °F : -00 °C /212 °F 100 °C /221 °F : -100 °C /220 °F 100 °C /220 °F : -00 °C /200 °F Disabled ::::::::::::::::::::::::::::::::::::		Aptio Setup – AMI	
System Main Board Temperature : +39 °C i-Module Temperature : N/A CPU(PECI) Temperature : +31 °C System Power Temperature : +31 °C CPU(PECI) ACPI Shutdown Temperature : i-Module SYSFAN2 Speed +12VIN +SVIN +V9AT Fan Configuration Case Open Warning Temper CPU(PECI) Warning Temper CPU(PECI) Marning Temper CPU(PECI) Mar	Advanced		
Case Open Warning Watchdog Timer CPU(PECI) Warning Temper CPU(PECI) ACPI Shutdown Temperature	Pc Health Status System Main Board Temperati-Module Temperature CPU(PECI) Temperature System Power Temperature i-Module SYSFAN1 Speed i-Module SYSFAN2 Speed +12VIN +5VIN +5VIN	: N/A : +31 °C — CPU(PECI) ACPI Shutdown Temperatu 60°C/140°F 65°C/149°F 70°C/158°F 75°C/167°F 80°C/176°F	Temperature.
F4: Save & Exit ESC: Exit	Case Open Warning Watchdog Timer CPU(PECI) Warning Temper CPU(PECI) ACPI Shutdown	90°C/194°F 95°C/203°F 100°C/212°F 105°C/221°F 110°C/230°F	ct Item elect nge Opt. ral Help ious Values
			F4: Save & Exit
Version 2.22.1288 Copyright (C) 2023 AMI		Version 2.22.1288 Copyright (C) 202	3 AMI

Fan Configuration

Select Manual Mode or SmartFan Mode for the i-Module FAN.

Advanced	Aptio Setup – AMI	
Fan Configuration		Fan control mode select
i-Module Fan	[SmartFan Mode]	<pre>++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit</pre>
Ver	sion 2.22.1288 Copyright (C) 20	23 AMI



Advanced	Aptio Setup — AMI	
Fan Configuration		Select System Fan Control Option.
i-Module Fan PWM Output Value	[Manual Mode] 50	
		<pre>++: Select Screen fl: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit</pre>
Versi	ion 2.21.1278 Copyright (C) 202	21 AMI

3.2.2.11 S5 RTC Wake Settings

Advanced	Aptio Setup – AMI	
Wake system from S5	[Disabled]	Enable or disable System wake on alarm event. Select FixedTime, system will wake on the hr::min::sec specified. Select DynamicTime , System will wake on the current time + Increase minute(s)
	Version 2.22.1288 Copyright (C) :	2023 AMI

Figure 3.28 S5 RTC Wake Settings

Wake System at a Fixed Time

Enable/Disable system wake-on-alarm event. The system will wake on the hr:min:sec specified.

3.2.2.12 Serial Port Console Redirection

	Aptio Setup – AMI	
Advanced		
COM1 Console Redirection ▶ Console Redirection Settings	[Disabled]	Console Redirection Enable or Disable.
COM1(Pci Bus0,Dev0,Func0) (Disabled) Console Redirection	Port Is Disabled	
Legacy Console Redirection ▶ Legacy Console Redirection Settings		
Serial Port for Out-of-Band Managemen Windows Emergency Management Service:	s (EMS)	
Console Redirection EMS ▶ Console Redirection Settings	[Disabled]	<pre>++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit</pre>
Version 2	.22.1288 Copyright (C) 2023	AMI



Advanced	Aptio Setup – AMI	
Legacy Console Redirection Settings		Select a COM port to display
Redirection COM Port Resolution Redirect After POST	[СОМ1] [80x24] [Always Enable]	redirection of Legacy OS and Legacy OPROM Messages
		++: Select Screen 14: Select Item Enter: Select +/-: Change Opt.
		F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version 2	.22.1288 Copyright (C) 2023	AMI

Figure 3.30 Legacy Console Redirection Settings

- COM1
 - Console Redirection Settings
 - Enable/Disable console redirection.

- Legacy Console Redirection
 - Legacy Console Redirection Settings

Select a COM port to display redirection of legacy OS and legacy OPROM messages.

- Serial Port for Out-of-Band Management / Windows Emergency Management Services (EMS)
 - Console Redirection Settings
 Enable/Disable console redirection.

3.2.2.13 Intel[®] TXT Information

Advanced	Aptio Setup – AMI	
Intel TXT Information		
Chipset BiosAcm Chipset Txt Cpu Txt Error Code Class Code Major Code Minor Code	Production Fused Debug Fused Supported None None None None	<pre>+*: Select Screen fl: Select Item Enter: Select +/-: Change Opt. Fl: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit</pre>
V	ersion 2.22.1288 Copyright (C) 2	023 AMI

Figure 3.31 Intel[®] TXT Information

3.2.2.14 USB Configuration

Advanced	Aptio Setup – AMI		
USB Configuration		Enables Legacy USB support.	
USB Module Version	31	AUTO option disables legacy support if no USB devices are connected. DISABLE option will	
USB Controllers: 1 XHCI		keep USB devices available only for EFI applications.	
USB Devices: 1 Drive, 1 Keyboard, 2 Mice			
Legacy USB Support XHCI Hand-off	[Enabled] [Enabled]		
USB Mass Storage Driver Support	[Enabled]		
USB hardware delays and time-outs:		++: Select Screen	
USB transfer time-out	[20 sec]	↑↓: Select Item	
Device reset time–out Device power–up delay	[20 sec] [Auto]	Enter: Select +/-: Change Opt.	
Device power-up delay	[Huto]	F1: General Help	
Mass Storage Devices:		F2: Previous Values	
Sony Storage Media PMAP	[Auto]	F3: Optimized Defaults	
		F4: Save & Exit	
		ESC: Exit	
Version 2.22.1288 Copyright (C) 2023 AMI			
Figure 3 32 USB Configuration			

Figure 3.32 USB Configuration

Legacy USB Support

This is to support USB devices under legacy OS such as DOS. When you choose "AUTO", the system will automatically detect if any USB device is plugged into the computer and enable USB legacy mode when a USB device is plugged in, and disable USB legacy mode when no USB device is plugged in.

XHCI Hand-off

This is a workaround for OS without XHCI hand-off support. The XHCI ownership change should be claimed by the XHCI driver.

USB Mass Storage Driver Support

Enable/Disable USB Mass Storage driver support.

USB transfer time-out

Allows you to select the USB transfer time-out value. [1,5,10,20 sec]

Device reset time-out

Allows you to select the USB device reset time-out value. [10,20,30,40 sec]

Device power-up delay

This is the maximum time the device will take before it properly reports itself to the Host Controller. "Auto" uses the default value: for a Root port it is 100 ms, for a Hub port the delay is taken from the Hub descriptor.

Mass Storage Devices

This is the mass storage device emulation type. "Auto" enumerates the device according to its media format. Optical drives are emulated as "CD-ROM" drives with no media and will be emulated according to a drive type.

Chapter 3 BIOS Operation

3.2.2.15 Network Stack Configuration

Advanced	Aptio Setup — AMI	
Network Stack	[Disabled]	Enable/Disable UEFI Network Stack ++: Select Screen 1: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
	Version 2.22.1288 Copyright (C	:) 2023 AMI

Figure 3.33 Network Stack Configuration

Network Stack

Enable/Disable the UEFI Network Stack.

3.2.2.16 CSM Configuration

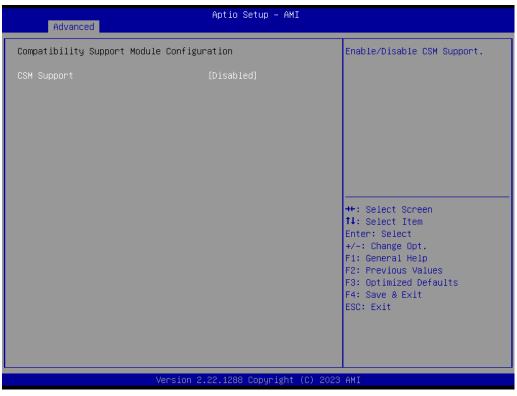


Figure 3.34 CSM Configuration

Compatibility Support Module Configuration

- CSM Support

Enable/Disable CSM Support. The default setting is "Disabled". If your graphics card does not support UEFI mode, make sure to select "Enabled" to allow non-UEFI boot mode before installing the graphics card to turn on the computer.

3.2.2.17 NVMe Configuration



Figure 3.35 NVMe Configuration

NVMe Configuration

NVMe M.2 storage devices are supported.

3.2.3 Chipset



Figure 3.36 Chipset

This page provides information for the chipset on MIC-770 V3.

Chapter 3 BIOS Operation

3.2.3.1 System Agent (SA) Configuration

Chipset	Aptio Setup – AMI	
System Agent (SA) Configuration		Memory Configuration Parameters
VT-d	Supported	
 Memory Configuration Graphics Configuration VMD setup menu PCI Express Configuration 		
VT-d Above 4GB MMIO BIOS assignment	[Enabled] [Enabled]	
		<pre>++: Select Screen fl: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit</pre>
Version	2.22.1288 Copyright (C) 2023	AMI

Figure 3.37 System Agent (SA) Configuration

- VT-d Enable/Disable VT-d function.
- Above 4GB MMIO BIOS assignment
 Enable/Disable above 4GB MemoryMapped IO BIOS assignment.

3.2.3.2 Memory Configuration



Figure 3.38 Memory Configuration

Maximum Memory Frequency

Maximum memory frequency selection in MHz.

Chapter 3 BIOS Operation

3.2.3.3 Graphics Configuration



Figure 3.39 Graphics Configuration

Primary Display

Set Primary Display to "Auto", "IGFX", "PEG", "PCI", or "SG".

Internal Graphics

Set Internal Graphics to "Auto", "Disable", or "Enable". "Auto" will disable internal graphics when a GPU card is installed. If GPU and internal graphics outputs are required at the same time, set this item to "Enable".

3.2.3.4 PEG Port Configuration

Chipset	Aptio Setup – AMI	
PEG Port Configuration		Enable or Disable the Root Port
<pre>PEG 0:1:0 Enable Root Port Max Link Speed PEG 0:1:1 Enable Root Port Max Link Speed PEG 0:1:2 Enable Root Port Max Link Speed</pre> PEG Port Feature Configuration	Not Present [Auto] [Auto] Not Present [Auto] [Auto] Not Present [Auto] [Auto]	
		<pre>++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit</pre>
Version 2.21.1278 Copyright (C) 2021 AMI		

Figure 3.40 PEG Port Configuration

Aptio Setup Utility - Chipset	- Copyright (C) 2016 American	Megatrends, Inc.
PEG Port Feature Configuration		Detect Non-Compliance PCI Express Device in PEG
Detect Non-Compliance Device	(Disabled)	
		++: Select Screen ↑↓: Select Item
		Enter: Select +/-: Change Opt. F1: General Help
		F2: Previous Values F3: Optimized Defaults
		F4: Save & Exit ESC: Exit
	Copyright (C) 2016 American M	
Figure 3.41 F	PEG Port Feature Cor	nfiguration

- Enable Root Port
 Enable/Disable the root port.
- Max Link speed
 Configure PEG 0:1:0 max speed.

PEG Port Feature Configuration

- Detect Non-Compliance Device

Detect a non-compliant PCI Express device in PEG. If enabled, it will take more time for POST.

3.2.3.5 PCH-IO Configuration





LAN1 Controller

Enable/Disable LAN1 controller.

- LAN1 Option-ROM Enable/Disable LAN1 boot option for legacy network devices.
- LAN2 Controller Enable/Disable LAN2 controller.
- LAN2 Option-ROM

Enable/Disable LAN2 boot option for legacy network devices.

PCIE Wake

Enable/Disable PCIE to wake the system from S5. When this item is selected as "Disabled", the Wake-on-LAN2 function is also disabled.

PowerOn by Modem
 Enable/Disable PowerOn by Modem.

Restore AC Power Loss Behavior when recovering from AC power loss: "S0" (power on), "S5" (power off), or "Last State".

PCIE Device Initial Delay Users can set the number of seconds to delay the PCIE device initialization.

3.2.3.6 PCI Express Configuration

Chipset	Aptio Setup – AMI	
PCI Express Configuration	A	PCI Express Root Port Settings.
 PCI Express Root Port 1 PCI Express Root Port 2 PCI Express Root Port 3 PCI Express Root Port 4 PCI Express Root Port 5 PCI Express Root Port 6 PCI Express Root Port 7 PCI Express Root Port 8 PCI Express Root Port 9 PCI Express Root Port 9 PCI Express Root Port 10 PCI Express Root Port 11 PCI Express Root Port 12 PCI Express Root Port 13 PCI Express Root Port 14 PCI Express Root Port 15 PCI Express Root Port 16 PCI Express Root Port 16 PCI Express Root Port 17 PCI Express Root Port 16 PCI Express Root Port 17 PCI Express Root Port 17 PCI Express Root Port 18 PCI Express Root Port 19 	Shadowed by x2/x4 port Shadowed by x2/x4 port Shadowed by x2/x4 port Lane configured as USB/SATA/UFS Shadowed by x2/x4 port Shadowed by x2/x4 port	<pre>++: Select Screen t↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit</pre>
	Version 2.22.1288 Copyright (C) 2023	AMT

Chipset	Aptio Setup - AMI
PCI Express Root Port 10 PCI Express Root Port 11 PCI Express Root Port 12 PCI Express Root Port 13 PCI Express Root Port 14 PCI Express Root Port 15 PCI Express Root Port 16 PCI Express Root Port 17 PCI Express Root Port 18 PCI Express Root Port 18 PCI Express Root Port 19	Shadowed by x2/x4 port Shadowed by x2/x4 port Shadowed by x2/x4 port Lane configured as USB/SATA/UFS Lane configured as USB/SATA/UFS Lane configured as
PCI Express Root Port 20 PCI Express Root Port 21 PCI Express Root Port 22 PCI Express Root Port 23 PCI Express Root Port 24 PCI Express Root Port 25 PCI Express Root Port 26 PCI Express Root Port 27 PCI Express Root Port 28	USB/SATA/UFS Lane configured as USB/SATA/UFS ++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults Shadowed by x2/x4 port Shadowed by x2/x4 port Shadowed by x2/x4 port
▶ PCIE clocks	Version 2.22.1288 Copyright (C) 2023 AMI

Figure 3.43 PCI Express Configuration

Aptio Setup Util. Chipset	ity – Copyright (C) 2018 (American Megatrends, Inc.
PCI Express Root Port 1 PCIe Speed	[Enabled] [Auto]	Control the PCI Express Root Port.
		<pre>++: Select Screen fl: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit</pre>
Version 2.20.12	71. Copyright (C) 2018 Am	erican Megatrends. Inc.

Figure 3.44 PCI Express Root Port

 PCI Express Root Port 1 Enable/Disable PCI Express Root Port.

PCIe Speed

Select "Auto, Gen1, Gen2, Gen 3" for PCIe Speed.

3.2.3.7 SATA Configuration

Chipset	Aptio Setup — AMI	
Hot Plug[Enablic configured as eSATAHot FConfigured as eSATAHot FSpin Up Device[Disaligned configured configured configured configured configured configured configured configured as eSATAEmptyHot Plug[Enablic configured configur	[Enabled] Hot Plug supported [Disabled] [Hard Disk Drive] Empty Unknown [Enabled] [Enabled] Hot Plug supported [Disabled] [Hard Disk Drive] Empty Unknown [Enabled] [Enabled] [Enabled] Hot Plug supported	 Identify the SATA port is connected to Solid State Drive or Hard Disk Drive ++: Select Screen 11: Select Item Enter: Select
Spin Up Device SATA Device Type Serial ATA Port 4 Software Preserve Port 4 Hot Plug Configured as eSATA Spin Up Device SATA Device Type	[Disabled] [Hard Disk Drive] Empty Unknown [Enabled] [Enabled] Hot Plug supported [Disabled] [Hard Disk Drive]	+/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit

Figure 3.45 SATA Configuration

- SATA Controller(s) Enable/Disable SATA controller.
 SATA Mode Selection This can be configured as "RAID" or "AHCI".
- Ports 1~4 Enable/Disable SATA ports 1~4.
- Hot Plug
 Enable/Disable SATA Hot-Plug.
- Spin Up Device
 Enable/Disable spin up device.
- SATA Device Type

To identify the SATA that is connected to a "Solid State Drive" or "Hard Disk Drive".

3.2.3.8 VMD Setup Menu

Chipset	Aptio Setup – AMI	
VMD Configuration		Enable/Disable to VMD
Enable VMD controller	[Enabled]	controller
Enable VMD Global Mapping Map this Root Port under VMD Root Port BDF details RATDO	[Enabled] [Disabled] SATA Controller [Enabled]	
RAIDO RAIDO RAIDO RAIDOO Intel Rapid Recovery Technology	[Enabled] [Enabled] [Enabled] [Enabled]	
RRT volumes can span internal and eSATA drives Intel(R) Optane(TM) Memory		++: Select Screen fl: Select Item Enter: Select
		+/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version	2.22.1288 Copyright (C) 200	23 AMI

Chipset	Aptio Setup — AMI	
VMD Configuration		Enable/Disable to VMD controller
Enable VMD controller	[Disabled]	
		++: Select Screen
		<pre>\$</pre>
		F1: General Help F2: Previous Values F3: Optimized Defaults
		F4: Save & Exit ESC: Exit
	Version 2.22.1288 Copyright (C)	2023 AMI

Figure 3.46 VMD Controller

Enable/Disable VMD Controller

Enable/Disable VMD controller. Enable the VMD controller to activate the "Intel Rapid Storage Technology" option and set up RAID.

3.2.3.9 USB Configuration

Advanced	Aptio Setup – AMI	
USB Configuration		Enables Legacy USB support.
USB Module Version	31	AUTO option disables legacy support if no USB devices are connected. DISABLE option will
USB Controllers: 1 XHCI		keep USB devices available only for EFI applications.
USB Devices: 1 Drive, 1 Keyboard, 2 Mice		
Legacy USB Support XHCI Hand-off	[Enabled] [Enabled]	
USB Mass Storage Driver Support	[Enabled]	
USB hardware delays and time-outs:		++: Select Screen
USB transfer time-out Device reset time-out	[20 sec]	↑↓: Select Item
Device power-up delay	[20 sec] [Auto]	Enter: Select +/-: Change Opt.
pevice power up deidy	[lid(0]	F1: General Help
Mass Storage Devices:		F2: Previous Values
Sony Storage Media PMAP	[Auto]	F3: Optimized Defaults
		F4: Save & Exit
		ESC: Exit
Version 2	2.22.1288 Copyright (C) 2023	AMI

Figure 3.47 USB Configuration

XHCI Compliance Mode

Enable/Disable XHCI compliance mode. The default is to disable compliance mode.

Chapter 3 BIOS Operation

3.2.3.10 Security Configuration

Chipset	Aptio Setup – AMI	
Security Configuration RTC Memory Lock BIOS Lock Force unlock on all GPIO pads	[Enabled] [Enabled] [Disabled]	Enable will lock bytes 38h–3Fh in the lower∕upper 128–byte bank of RTC RAM
		<pre>++: Select Screen f↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values</pre>
		F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version	n 2.22.1288 Copyright (C)	2023 AMI

Figure 3.48 Security Configuration

RTC Memory Lock

Enable will lock bytes 38h-3Fh in the lower/upper 128-byte bank of RTC RAM.

BIOS Lock

Enable/Disable the PCH BIOS Lock Enable feature. It is required to be enabled to ensure SMM protection of flash.

Force unlock on all GPIO pads If enabled, the BIOS will force all GPIO pads to be in the unlocked state.

3.2.3.11 HD Audio Configuration



Figure 3.49 HD Audio Configuration

HD Audio

Control detection of the HD-Audio device. Disable = HDA will be unconditionally disabled. Enable = HDA will be unconditionally enabled.

3.2.4 Security

Aptio Setup – AMI Main Advanced Chipset <mark>Security</mark> Boot Save & Exit MEBx		
Password Description		Set Administrator Password
If ONLY the Administrator's then this only limits access only asked for when entering If ONLY the User's password is a power on password and n boot or enter Setup. In Setu have Administrator rights. The password length must be in the following range:	s to Setup and is g Setup. is set, then this must be entered to up the User will	
Minimum length Maximum length	3 20	++: Select Screen
Haximan iengen	20	↑↓: Select Item
Administrator Password		Enter: Select
User Password		+/−: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit
▶ Secure Boot		ESC: Exit
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Figure 3.50 Security

Select Security Setup from the MIC-770 V3 setup Main BIOS setup menu. All Security setup options, such as password protection are described in this section. To access the sub-menu for the following items, select the item and press <Enter>.



If only the user password is set, the user will have administrator rights. Setting an administrator password is strongly recommended if you have security concerns.

3.2.5 Boot

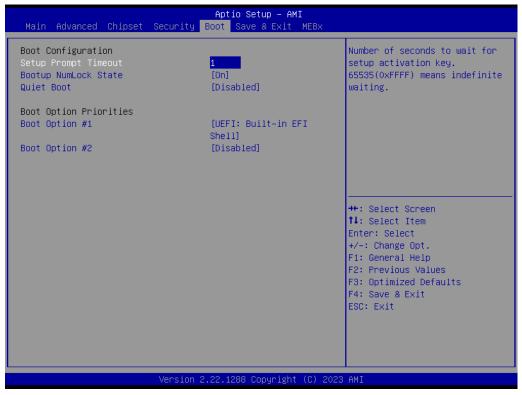


Figure 3.51 Boot

Setup Prompt Timeout

Directly key in the number, or use the <+> and <-> keys to adjust the number of seconds to wait for the setup activation key.

Bootup NumLock State

Default state for the NumLock key during power on.

Quiet Boot

Enable/Disable the Quiet Boot option. When enabled, the BIOS logo will show in place of the POST screen.

Boot Option Priorities

Set the boot order.

3.2.6 Save & Exit

Aptio Setup – AMI Main Advanced Chipset Security Boot <mark>Save & Exit</mark> MEBx		
Save Options Save Changes and Exit Discard Changes and Exit Save Changes and Reset Discard Changes and Reset Save Changes Discard Changes Default Options Restore Defaults Save as User Defaults	Exit system setup after saving the changes.	
Restore User Defaults Boot Override UEFI: Built-in EFI Shell	<pre>++: Select Screen f↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit</pre>	
Version 2.22.1288 Copyright (C) 2023 AMI		

Figure 3.52 Save & Exit

Save Changes and Exit

When you complete system configuration, select this option to save your changes, exit BIOS setup, and reboot the computer so the new system configuration parameters can take effect.

1. Select Exit Saving Changes from the Exit menu and press <Enter>. The following message appears:

Save Configuration Changes and Exit Now?

[Yes] [No]

2. Select Yes or No.

Discard Changes and Exit

Select this option to quit Setup without making any permanent changes to the system configuration.

1. Select Exit Discarding Changes from the Exit menu and press <Enter>. The following message appears:

Quit without saving?

[Yes] [No]

2. Select Yes to discard changes and exit.

Discard Changes

Select Discard Changes from the Exit menu and press <Enter>.

MIC-770 V3 User Manual



Software Installation

This chapter introduces driver installation.

4.1 Before You Begin

To facilitate the installation of the enhanced display drivers and utility software, read the instructions in this chapter carefully. The drivers for the MIC-770 V3 are located on the Advantech support website (http://www.advantech.com/support). Updates are provided via Service Packs from Microsoft.



For system stability, installing the drivers in the following sequence is highly recommended:



- Chipset
 Graphics
- Graphics
- ME
- Other drivers

Before you begin, it is important to note that most display drivers need to have the relevant software application already installed in the system prior to installing the enhanced display drivers. In addition, many of the installation procedures assume that you are familiar with both the relevant software applications and operating system commands. Review the relevant operating system commands and the pertinent sections of your application software's user manual before performing the installation.

4.2 Introduction

The Intel[®] Chipset Software Installation (CSI) utility installs the Windows INF files that outline to the operating system how the chipset components will be configured. This is needed for the proper functioning of the following features:

- Core PCI PnP services
- Serial ATA interface support
- Identification of Intel chipset components in the Device Manager.



The chipset driver is used for the following versions of Windows, and it has to be installed before installing all the other drivers:

Windows 10 (64-bit)

4.3 Windows Driver Setup

Enter the Advantech support website, then search for MIC-770 V3. There you can find the graphics drivers for MIC-770 V3.



Integrated Graphics Device Setup

5.1 Introduction

The Intel[®] processors are embedded with an integrated graphics controller. You need to install the VGA driver to enable this function. This driver provides the graphics, compute, media, and display capabilities.

5.2 Windows Driver Setup



Before installing this driver, make sure the INF driver has been installed in your system. See Chapter 4 for information on installing the INF driver.

Enter the Advantech support website, then search for MIC-770 V3. You can find the "Graphics" driver there.



Intel[®] ME

6.1 Introduction

The Intel[®] ME software components that need to be installed depend on the system's specific hardware and firmware features. The installer detects the system's capabilities and installs the relevant drivers and applications.

6.2 Installation

Enter the Advantech support website, then search for MIC-770 V3. You can find the "ME" drivers there.



LAN Configuration

7.1 Introduction

The MIC-770 V3 has dual Gigabit Ethernet LANs via dedicated PCI Express x1 lanes (Intel[®] I219LM (LAN1) and I210IT (LAN2)). These offer bandwidth of up to 500 MB/ sec, thus eliminating network data flow bottlenecks, and incorporating Gigabit Ethernet at 1000 Mbps.

7.2 Features

- 10/100/1000 Base-T Ethernet controller
- 10/100/1000 Base-T triple-speed MAC
- Full duplex at 10, 100, or 1000 Mbps and half duplex at 10 or 100 Mbps
- Wake-on-LAN (WOL) support
- PCIe x1 host interface

7.3 Installation



Before installing the LAN drivers, make sure the CSI utility has been installed on your system. See Chapter 4 for information on installing the CSI utility.

The integrated Intel[®] gigabit Ethernet controller supports all major network operating systems. However, the installation procedure varies with different operating systems. In the following sections, refer to the one that provides the driver setup procedure for the operating system you are using.

7.4 Windows Driver Setup

Enter the Advantech support website, then search for MIC-770 V3. You can find the "LAN" driver there.



SATA RAID Setup

8.1 Introduction

The Intel[®] R680E chipset integrates 4 x Serial ATA controllers with software for RAID 0, 1, 5, 10 capabilities to support demanding disk I/O.

RAID 0 striping increases storage performance and is designed to speed up data transfer rates for disk-intensive applications.

RAID 1 mirroring protects valuable data that might be lost in the event of a hard drive failure.

The RAID 5 array contains three or more hard drives where the data is divided into manageable blocks called strips. Parity is a mathematical method for recreating data that was lost from a single drive, which increases fault tolerance. The data and parity are striped across all the hard drives in the array. The parity is striped in a rotating sequence to reduce bottlenecks associated with the parity calculations.

The RAID 10 array uses four hard drives to create a combination of RAID levels 0 and 1. The data is striped across a two-drive array forming the RAID 0 component. Each of the drives in the RAID 0 array is then mirrored by a RAID 1 component.

8.2 SATA RAID Driver and Utility Setup

Enter the Advantech support website, then search for MIC-770 V3. You can see the "Others" folder containing the RST driver.



HD Audio

9.1 Introduction

MIC-770 V3 is equipped with a Realtek ALC888S audio chip. It provides "Line-out" & "Microphone" ports for diverse applications.

9.2 Installation

Enter the Advantech support website, then search for MIC-770 V3. This is where users can find the "Audio" drivers.



Programming the Watchdog Timer

A.1 Programming the Watchdog Timer

The MIC-770 V3's watchdog timer can be used to monitor system software operation and take corrective action if the software fails to function within the programmed period. This section describes the operation of the watchdog timer and how to program it.

A.1.1 Watchdog Timer Overview

The watchdog timer is built into the super I/O controller NCT6126D. It provides the following user-programmable functions:

- It can be enabled and disabled by user configuration.
- The timer can be set from 1 to 255 seconds or 1 to 255 minutes.
- It generates an interrupt or reset signal if the software fails to reset the timer before time-out.

A.1.2 Programming the Watchdog Timer

The I/O port address of the watchdog timer is 2E (hex) and 2F (hex). 2E (hex) is the address port. 2F (hex) is the data port. You must first assign the address of the register by writing an address value into address port 2E (hex), then write/read data to/ from the assigned register through data port 2F (hex).

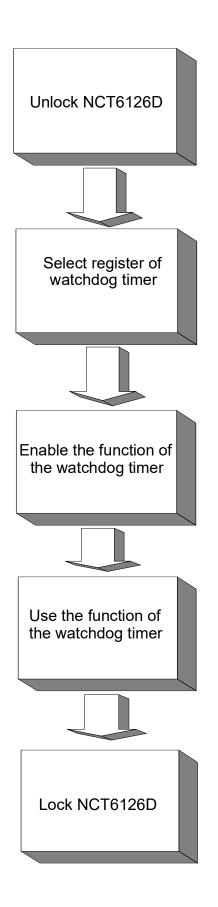


Table A.1: Watchdog	Timer Regi	isters
Address of Register (2E)	Attribute	
Read/Write	Value (2F) & description	
87 (hex)		Write this address to I/O address port 2E (hex) twice to unlock the NCT6126D.
07 (hex)	write	Write 08 (hex) to select the register of the watch- dog timer.
30 (hex)	write	Write 01 (hex) to enable the function of the watch- dog timer. Disabled is set as default.
F0 (hex)	write	Set seconds or minutes as units for the timer. Write 0 to bit 3: set seconds as the counting unit. [default] Write 1 to bit 3: set minutes as the counting unit.
F1 (hex)	write	0: stop timer [default] 01~FF (hex): The amount of the count, in seconds or minutes, depends on the value set in register F5 (hex). This number decides how long the watch- dog timer waits for the strobe before generating an interrupt or reset signal. Writing a new value to this register can reset the timer to count with the new value.
F2 (hex)	read/write	Bit 7: Write 1 to enable the mouse to reset the timer, 0 to disable [default]. Bit 6: Write 1 to enable the keyboard to reset the timer, 0 to disable. [default] Bit 5: Write 1 to generate a timeout signal immedi- ately and automatically return to 0. [default=0] Bit 4: Read status of the watchdog timer, 1 means the timer is "timeout".
AA (hex)		Write this address to I/O port 2E (hex) to lock watchdog timer 2.

Appendix A Programming the Watchdog Time

A.1.3 Example Program

Out dx,al

1. Enable watchdog timer and set 10 sec. as the timeout interval.

:-----

Mov dx,2eh ; Unlock NCT6126D Mov al,87h Out dx,al Out dx.al :-----Mov al,07h ; Select registers of the watchdog timer. Out dx,al Inc dx Mov al,08h Out dx,al :-----Dec dx ; Enable the function of watchdog timer. Mov al,30h Out dx,al Inc dx Mov al,01h Out dx,al ;-----Dec dx ; Set seconds as the counting unit. Mov al,0f0h Out dx,al Inc dx In al,dx And al, not 08h Out dx,al :-----Dec dx ; Set the timeout interval as 10 seconds and start counting. Mov al,0f1h Out dx,al Inc dx Mov al,10 Out dx.al ;-----Dec dx ; Lock NCT6126D Mov al,0aah Out dx,al Enable the watchdog timer and set 5 minutes as the timeout interval. 2. ;-----Mov dx,2eh ; Unlock NCT6126D Mov al,87h Out dx,al

._____ Mov al,07h ; Select registers of the watchdog timer. Out dx,al Inc dx Mov al,08h Out dx,al ;-----Dec dx ; Enable the function of the watchdog timer. Mov al,30h Out dx,al Inc dx Mov al,01h Out dx,al ;-----Dec dx ; Set minutes as the counting unit. Mov al,0f0h Out dx,al Inc dx In al,dx Or al,08h Out dx,al :-----_____ Dec dx ; Set the timeout interval as 5 minutes and start counting. Mov al,0f1h Out dx,al Inc dx Mov al.5 Out dx,al :-----Dec dx ; Lock NCT6126D Mov al,0aah Out dx,al 3. Enable the watchdog timer to be reset by mouse. :-----Mov dx,2eh ; Unlock NCT6126D Mov al.87h Out dx,al Out dx,al ;-----Mov al,07h ; Select registers of the watchdog timer. Out dx,al Inc dx Mov al,08h Out dx,al -----

Dec dx ; Enable the function of the watchdog timer. Mov al,30h Out dx,al Inc dx Mov al,01h Out dx,al ;-----Dec dx ; Enable the watchdog timer to be reset by mouse. Mov al,0f2h Out dx,al Inc dx In al.dx Or al,80h Out dx,al :-----Dec dx ; Lock NCT6126D Mov al,0aah Out dx,al 4. Enable the watchdog timer to be reset by keyboard. ;-----Mov dx,2eh ; Unlock NCT6126D Mov al,87h Out dx,al Out dx,al ;-----Mov al,07h ; Select registers of the watchdog timer. Out dx,al Inc dx Mov al,08h Out dx,al ;-----Dec dx ; Enable the function of the watchdog timer. Mov al,30h Out dx,al Inc dx Mov al.01h Out dx,al :-----Dec dx ; Enable the watchdog timer to be strobe reset by keyboard. Mov al,0f2h Out dx,al Inc dx In al,dx Or al,40h Out dx,al

:-----Dec dx ; Lock NCT6126D Mov al,0aah Out dx,al 5. Generate a time-out signal without the timer counting. :-----Mov dx,2eh ; Unlock NCT6126D Mov al,87h Out dx,al Out dx,al ;-----Mov al,07h ; Select registers of the watchdog timer, Out dx,al Inc dx Mov al,08h Out dx,al Dec dx ; Enable the function of the watchdog timer. Mov al,30h Out dx,al Inc dx Mov al,01h Out dx,al ;<u>-----</u> Dec dx ; Generate a time-out signal. Mov al,0f2h Out dx,al ;Write 1 to bit 5 of F7 register Inc dx In al,dx Or al,20h Out dx,al ;----------Dec dx ; Lock NCT6126D Mov al,0aah Out dx,al



Programming the GPIO

B.1 Supported GPIO Register

The following is a description of the GPIO addresses and programming sample.

B.1.1 GPIO Registers

GPIO 1

CRF0 (GP10-GP17 I/O selection register. Default 0xFF)

When set to '1', the respective GPIO port is programmed as an input port. When set to '0', the respective GPIO port is programmed as an output port.

CRF1 (GP10-GP17 data register. Default 0x00)

If a port is programmed to be an output port, then its respective bit can be read/written.

If a port is programmed to be an input port, then its respective bit can only be read.

CRF2 (GP10-GP17 inversion register. Default 0x00)

When set to '1', the incoming/outgoing port value is inverted. When set to '0', the incoming/outgoing port value is the same as in the data register.

GPIO 0

CREC (GP00-GP07 I/O selection register. Default 0xFF)

When set to '1', the respective GPIO port is programmed as an input port. When set to '0', the respective GPIO port is programmed as an output port.

CRED (GP00-GP07 data register. Default 0xFF)

If a port is programmed to be an output port, then its respective bit can be read/written.

If a port is programmed to be an input port, then its respective bit can only be read.

CREE (GP00-GP07 inversion register. Default 0x00)

When set to '1', the incoming/outgoing port value is inverted. When set to '0', the incoming/outgoing port value is the same as in the data register.

Extended Function Index Registers (EFIRs)

The EFIRs are write-only registers with port address 2Eh or 4Eh on PC/AT systems.

Extended Function Data Registers (EFDRs)

The EFDRs are read/write registers with port address 2Fh or 4Fh on PC/AT systems

B.1.2 GPIO Example Program

Enter the extended function mode, interruptible double-write

MOV DX, 2EH MOV AL, 87H OUT DX, AL OUT DX, AL _____ Configure logical device 7(GP10~GP17), configuration register CRE4,CRE5,CRE6 -----MOV DX, 2EH MOV AL, 07H; Point to Logical Device Number Reg. OUT DX, AL MOV DX, 2FH MOV AL, 07H ; Select logical device 7 OUT DX, AL -----Configure GPIO1 I/O Register _____ MOV DX, 2EH MOV AL, ECH OUT DX, AL MOV DX, 2FH MOV AL, ??H ; 0: The respective GPIO1 PIN is programmed as an output port ;1: The respective GPIO1 PIN is programmed as an input port. OUT DX, AL Configure GPIO1 Inversion Register MOV DX, 2EH MOV AL, EEH OUT DX, AL MOV DX, 2FH MOV AL, 00H ; Set GPIO is normal not inverter OUT DX, AL Configure GPIO1 Data Register _____ MOV DX, 2EH MOV AL, EDH

OUT DX, AL OUT DX, AL MOV DX, 2FH MOV AL, ??H ; Put the output value into AL OUT DX, AL

Exit extended function mode |

MOV DX, 2EH MOV AL, AAH OUT DX, AL _____



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