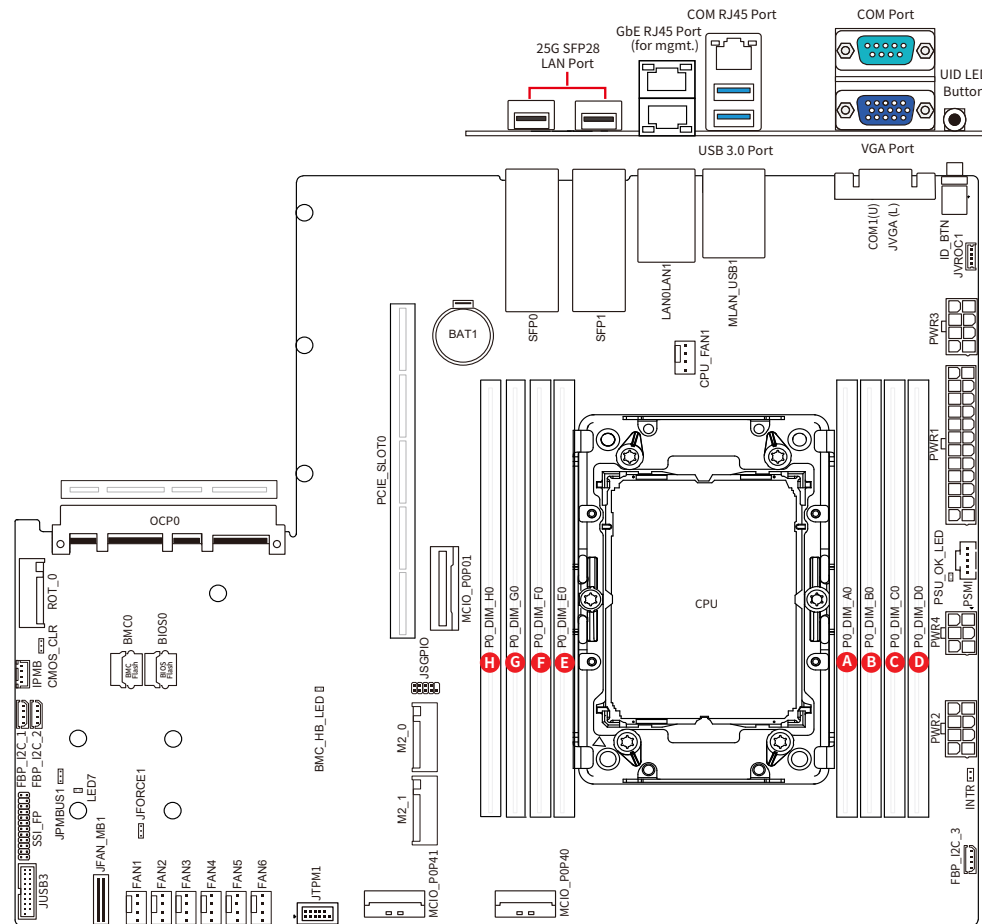


D3065/S3065

QUICK START GUIDE

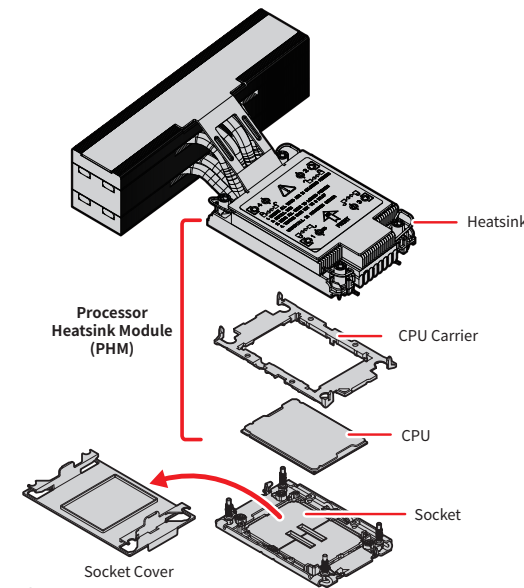


CPU

Single Intel® Xeon® 6700E-series, 6500P-series and 6700P-series processors(Socket E2)

Front Panel Connector

SSI_FP	1	POWER LED+	2	P3V3_AUX
	3	Key	4	SYS ID LED+
	5	FP_PWR_LED_B_N	6	FP_ID_LED_B_R_N
	7	HDD_ACT_LED+	8	SYS_ERR_LED_L
	9	FP_HDD_ACT_LED_N	10	SYS_FLT_LED_L
	11	FP_PWR_BTN_R_N	12	ACT LED+
	13	POWER SWITCH GND	14	FP_LNK_ACT_NIC1_LED_N
	15	SYS_RST_BTN	16	SMB_BMC_SCM_SDA_LVC3
	17	RESET SWITCH GND	18	SMB_BMC_SCM_SCL_LVC3
	19	FP_ID_BTN_N_R	20	FP_INTRUDER_N
	21	GND	22	ACT LED+
	23	FP1_NMI_BTN_N	24	FP_LNK_ACT_NIC2_LED_N



1. Check the **CPU** and the **CPU carrier** for their Pin 1 indicators.
2. Align their **Pin 1 indicators** before inserting the CPU into the CPU carrier. If installed properly, the CPU will snap into the side snap latches on the carrier and the carrier will latch firmly to the processor.
3. Remove the protective film on the base of heatsink if any.
4. Align Pin 1 indicator of CPU carrier and the corner cutout of the heatsink. Gently press the heatsink down to engage the carrier's latching mechanism to the heatsink at four corners. A clicking sound will be heard when latched properly.
5. Verify that the carrier and heatsink are firmly latched as one **Processor Heatsink Module (PHM)**.
6. Place the PHM on top of the motherboard CPU socket with Pin 1 indicators aligned.
7. Tighten all screws on the PHM in diagonal sequences to secure it to the motherboard.



Please refer to the following table for the corresponding CPU carrier.

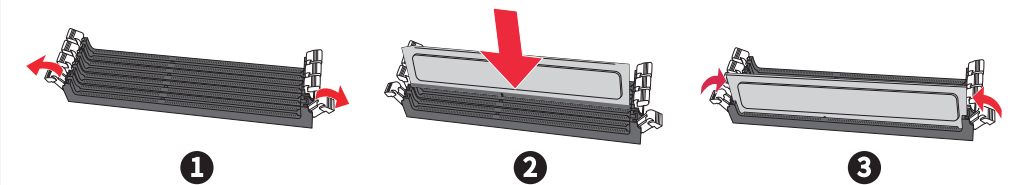
CPU Package Type	Granite Rapids-SP XCC	Granite Rapids-SP HCC Granite Rapids-SP LCC Sierra Forest-SP
CPU Carrier Code	E2A	E2B

Connectors, Jumpers and LED Indicators

Name	Description
PWR1	24-pin ATX power connector
PWR2,PWR3	8-pin ATX power connector
PWR4	6-pin HDD Backplane power connector
MCIO_P0P40	MCIO 8i connector
MCIO_P0P41	MCIO 8i connector
MCIO_P0P01	MCIO 8i connector
M2_0~1	M.2 slots (M Key, PCIe 5.0 x2, 22110/ 2280)
CPU_FAN	CPU fan power connector
ROT_0	M.2 port for Aspeed AST1060 hardware root of trust module connector
JUSB3	USB 3.0 connector
FBP_I2C_1~3	HDD BP SMBUS header
JFAN_MB1	FAN header
IPMB	IPMB header
PSMI	PMBus connector
JVROC1	VROC box header
JTPM1	TPM box header
INTR	chassis intrusion header
SSI_FP	24-pin front panel connector
PCIe_SLOT0	PCIe 5.0 x 32 slot (from CPU)
OCP0	OCP 3.0 SFF (PCIe 5.0 x16 from CPU)
JPMBUSW1	BMC to VRM PMBUS select jumper
CMOS_CLR	CMOS clear jumper
JFORCE1	CPLD Force Power on select jumper
BMC_HB_LED	BMC Heartbeat LED
LED7	SYS_ERR_LED
PSU_OK	ATX_PWROK LED

Memory Installation

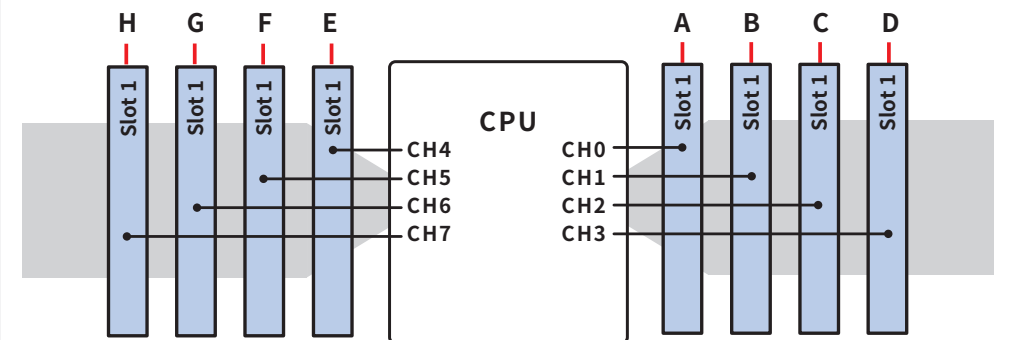
1. Open the side clips to unlock the DIMM slot.
2. Insert the DIMM vertically into the slot, ensuring that the off-center notch at the bottom aligns with the slot.
3. Push the DIMM firmly into the slot until it clicks and the side clips automatically close.
4. Verify that the side clips have securely locked the DIMM in place.



DDR5 Only DIMM Configuration

The server board offers compatibility with both **RDIMMs**, **3DS RDIMMs** and **MR DIMMs**.

DIMM Type	Max Frequency	Max Capacity per DIMM
RDIMM	6400 MT/s (1DPC)	128GB
3DS RDIMMs	6400 MT/s (1DPC)	256GB
MRDIMM	8000 MT/s (only support 1DPC)	64GB



SPR IMC#	IMC7	IMC6	IMC5	IMC4	C P U	IMC0	IMC1	IMC2	IMC3
Channel	H	G	F	E		A	B	C	D
	Chan 7	Chan 6	Chan 5	Chan 4		Chan 0	Chan 1	Chan 2	Chan 3
	H0	G0	F0	E0		A0	B0	C0	D0
1+0						V			
4+0		V		V		V		V	
	V		V				V		V
8+0	V	V	V	V		V	V	V	V
“V” indicates DIMMs are populated with DDR5 .									



- *There should be at least one DDR5 DIMM per socket.*

- When only one DIMM is used in a channel, it must be populated in the DIMM slot farthest away from the **CPU (DIMM slot 0)** of a given channel.

- *DDR5 memory configurations requires **same DIMM types, ranks, speeds, and densities.***